

Schematic Page List

Hide

Global Search

001

002

003

004

005

006

008

009

010

014

016

Toggle FullScreen

Pre Page

Next Page

MoveTo

--	--

--	--

009_CPU_CFG,RSVD

010_CPU_POWER_CAP

011_

012_

013_DDR4_

014_DDR4 SO-DIMM_A

015_DDR4_

016_DDR4_SO-DIMM_B

017_DDR4_****

018_DDR4_****

019_DDR4_CA_DQ_VOLTAGE

020_CPU_PCH_CSI2,EMMC

021_CPU_PCH_CGPIO, LPIO, MI
SC022_CPU_PCH_AUDIO,SDIO,SD
XC

023_CPU_PCH_PCIE,USB,SATA

024_CPU_PCH_CLOCK SIGNAL
S,RTC

025_CPU_PCH_SYS_POWER

026_CPU_PCH_POEWR,GND

027_CPU_PCH_POEWR,GND

X705 PCH_ GPIO

[illegible]

Full Name (Last, First, Middle)	DOB (MM/DD/YYYY)	English Name	Phone Number	Emergency Contact (Name & Phone)	Notes
John Doe	01/15/1990	John Doe	555-123-4567	John Doe (555-123-4567)	
Jane Smith	03/22/1985	Jane Smith	555-987-6543	Jane Smith (555-987-6543)	
Michael Johnson	07/08/1978	Michael Johnson	555-234-5678	Michael Johnson (555-234-5678)	
Emily Davis	11/30/1992	Emily Davis	555-345-6789	Emily Davis (555-345-6789)	
Robert Brown	05/12/1988	Robert Brown	555-456-7890	Robert Brown (555-456-7890)	
Sarah Wilson	09/03/1995	Sarah Wilson	555-567-8901	Sarah Wilson (555-567-8901)	
David Miller	12/01/1980	David Miller	555-678-9012	David Miller (555-678-9012)	
Amanda Taylor	06/18/1991	Amanda Taylor	555-789-0123	Amanda Taylor (555-789-0123)	
Christopher Lee	02/25/1983	Christopher Lee	555-890-1234	Christopher Lee (555-890-1234)	
Nicole White	08/14/1996	Nicole White	555-901-2345	Nicole White (555-901-2345)	
Kevin Black	04/07/1987	Kevin Black	555-012-3456	Kevin Black (555-012-3456)	
Stephanie Green	10/20/1993	Stephanie Green	555-123-4567	Stephanie Green (555-123-4567)	
Brandon Hall	01/05/1989	Brandon Hall	555-234-5678	Brandon Hall (555-234-5678)	
Michelle King	07/28/1994	Michelle King	555-345-6789	Michelle King (555-345-6789)	
Gregory Scott	03/11/1986	Gregory Scott	555-456-7890	Gregory Scott (555-456-7890)	
Heather Adams	09/24/1997	Heather Adams	555-567-8901	Heather Adams (555-567-8901)	
Timothy Baker	05/09/1982	Timothy Baker	555-678-9012	Timothy Baker (555-678-9012)	
Rebecca Nelson	11/17/1998	Rebecca Nelson	555-789-0123	Rebecca Nelson (555-789-0123)	
Jonathan Parker	06/02/1984	Jonathan Parker	555-890-1234	Jonathan Parker (555-890-1234)	
Christina Evans	12/19/1999	Christina Evans	555-901-2345	Christina Evans (555-901-2345)	
Benjamin Foster	08/05/1981	Benjamin Foster	555-012-3456	Benjamin Foster (555-012-3456)	
Victoria Gibson	04/21/1996	Victoria Gibson	555-123-4567	Victoria Gibson (555-123-4567)	
William Harris	10/08/1987	William Harris	555-234-5678	William Harris (555-234-5678)	
Olivia Young	07/23/1992	Olivia Young	555-345-6789	Olivia Young (555-345-6789)	
Lucas King	03/04/1985	Lucas King	555-456-7890	Lucas King (555-456-7890)	
Isabella Wright	09/16/1990	Isabella Wright	555-567-8901	Isabella Wright (555-567-8901)	
Matthew Lopez	05/27/1983	Matthew Lopez	555-678-9012	Matthew Lopez (555-678-9012)	
Chloe Hill	11/09/1997	Chloe Hill	555-789-0123	Chloe Hill (555-789-0123)	
Anthony Scott	06/20/1980	Anthony Scott	555-890-1234	Anthony Scott (555-890-1234)	
Madison Green	12/03/1994	Madison Green	555-901-2345	Madison Green (555-901-2345)	
Christopher Adams	08/12/1988	Christopher Adams	555-012-3456	Christopher Adams (555-012-3456)	
Grace Baker	04/29/1991	Grace Baker	555-123-4567	Grace Baker (555-123-4567)	
Robert Nelson	10/14/1986	Robert Nelson	555-234-5678	Robert Nelson (555-234-5678)	
Emily Parker	07/01/1993	Emily Parker	555-345-6789	Emily Parker (555-345-6789)	
Michael Evans	03/18/1989	Michael Evans	555-456-7890	Michael Evans (555-456-7890)	
Sarah Foster	09/05/1995	Sarah Foster	555-567-8901	Sarah Foster (555-567-8901)	
David Gibson	05/22/1982	David Gibson	555-678-9012	David Gibson (555-678-9012)	
Amanda Harris	11/07/1998	Amanda Harris	555-789-0123	Amanda Harris (555-789-0123)	
Kevin Young	06/24/1984	Kevin Young	555-890-1234	Kevin Young (555-890-1234)	
Stephanie King	12/11/1999	Stephanie King	555-901-2345	Stephanie King (555-901-2345)	
Brandon Wright	08/26/1981	Brandon Wright	555-012-3456	Brandon Wright (555-012-3456)	
Michelle Lopez	04/13/1996	Michelle Lopez	555-123-4567	Michelle Lopez (555-123-4567)	
Gregory Hill	10/30/1987	Gregory Hill	555-234-5678	Gregory Hill (555-234-5678)	
Heather Scott	07/17/1992	Heather Scott	555-345-6789	Heather Scott (555-345-6789)	
Timothy Adams	03/06/1985	Timothy Adams	555-456-7890	Timothy Adams (555-456-7890)	
Rebecca Baker	09/23/1990	Rebecca Baker	555-567-8901	Rebecca Baker (555-567-8901)	
Jonathan Nelson	05/10/1983	Jonathan Nelson	555-678-9012	Jonathan Nelson (555-678-9012)	
Christina Parker	11/25/1997	Christina Parker	555-789-0123	Christina Parker (555-789-0123)	
Benjamin Evans	06/08/1980	Benjamin Evans	555-890-1234	Benjamin Evans (555-890-1234)	
Victoria Foster	04/25/1994	Victoria Foster	555-901-2345	Victoria Foster (555-901-2345)	
William Gibson	10/12/1986	William Gibson	555-012-3456	William Gibson (555-012-3456)	
Olivia Harris	07/29/1991	Olivia Harris	555-123-4567	Olivia Harris (555-123-4567)	
Lucas Young	03/16/1988	Lucas Young	555-234-5678	Lucas Young (555-234-5678)	
Isabella King	09/03/1993	Isabella King	555-345-		



Schematic Page List

Hide

009_CPU_CFG,RSVD

010_CPU_POWER_CAP

011_

012_

013_DDR4_

014_DDR4_SO-DIMM_A

015_DDR4_

016_DDR4_SO-DIMM_B

017_DDR4_****

018_DDR4_****

019_DDR4_CA_DQ_VOLTAGE

020_CPU_PCH_CSI2,EMMC

021_CPU_PCH_CGPIO, LPIO, MI
SC

022_CPU_PCH_AUDIO,SDIO,SD
XC

023_CPU_PCH_PCIE,USB,SATA

024_CPU_PCH_CLOCK SIGNAL
S,RTC

025_CPU_PCH_SYS_POWER

026_CPU_PCH_POEWR,GND

027_CPU_PCH_POEWR,GND

Global Search

001

002

003

004

005

006

008

009

010

014

016

Toggle FullScreen

Pre Page

Next Page

MoveTo

Main Board

Display Port

A	EDP
B	N/A
C	HDMI

POD#543016 DD11 mapping DDPB
DD12 mapping DDPB

Compeation for D0/wDP/DDI Interface

ASUS Logo

Title : CPU DISPLAY

Engineer: EE

Scale: Custom

Project Name: X705UX

Rev: 2.0

Date: Wednesday, May 24, 2017

Sheet: 3 of 100

Schematic Page List

Hide

009_CPU_CFG,RSVD
010_CPU_POWER_CAP
011_
012_
013_DDR4_
014_DDR4_SO-DIMM_A
015_DDR4_
016_DDR4_SO-DIMM_B
017_DDR4_****
018_DDR4_****
019_DDR4_CA_DQ_VOLTAGE
020_CPU_PCH_CSI2,EMMC
021_CPU_PCH_CGPIO, LPIO, MI
SC
022_CPU_PCH_AUDIO,SDIO,SD
XC
023_CPU_PCH_PCIE,USB,SATA
024_CPU_PCH_CLOCK SIGNAL
S,RTC
025_CPU_PCH_SYS_POWER
026_CPU_PCH_POEWR,GND
027_CPU_PCH_POEWR,GND

Global Search

001

002

003

004

005

006

008

009

010

014

016

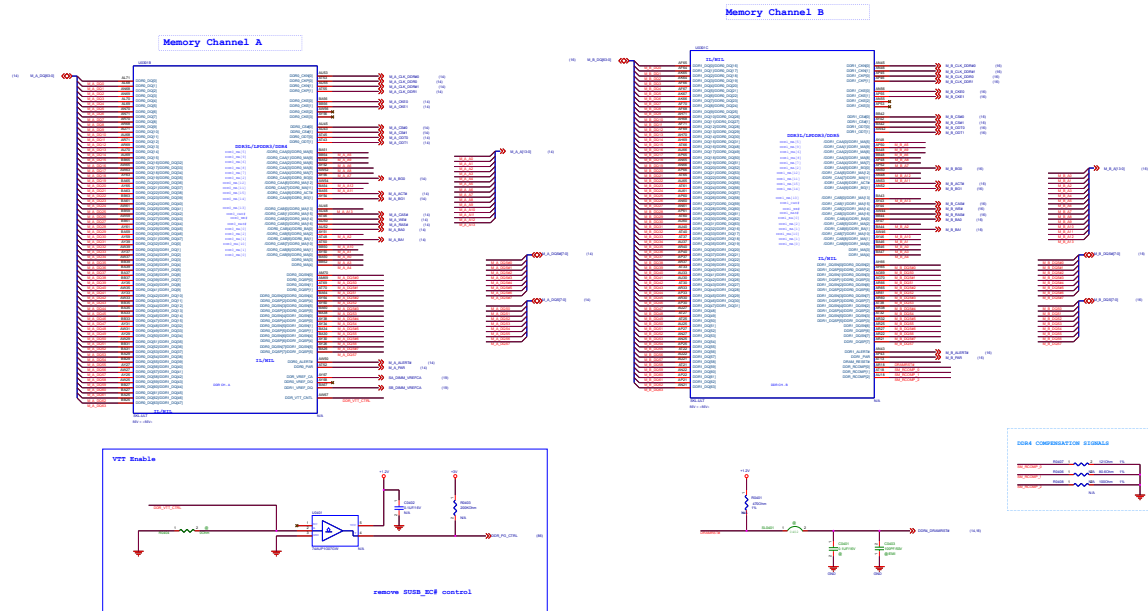
Toggle FullScreen

Pre Page

Next Page

MoveTo

Main Board



Hide

009_CPU_CFG,RSVD
010_CPU_POWER_CAP
011_
012_
013_DDR4_
014_DDR4_SO-DIMM_A
015_DDR4_
016_DDR4_SO-DIMM_B
017_DDR4_****
018_DDR4_****
019_DDR4_CA_DQ_VOLTAGE
020_CPU_PCH_CSI2,EMMC
021_CPU_PCH_CGPIO, LPIO, MI
SC
022_CPU_PCH_AUDIO,SDIO,SD
XC
023_CPU_PCH_PCIE,USB,SATA
024_CPU_PCH_CLOCK SIGNAL
S,RTC
025_CPU_PCH_SYS_POWER
026_CPU_PCH_POEWR,GND
027_CPU_PCH_POEWR,GND

Global Search

001

002

003

004

005

006

008

009

010

014

016

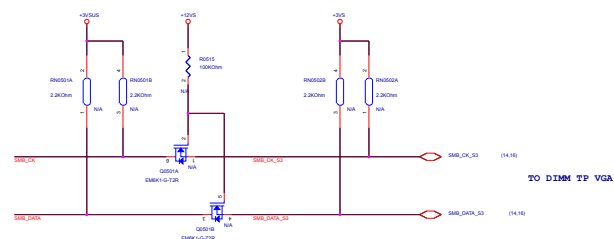
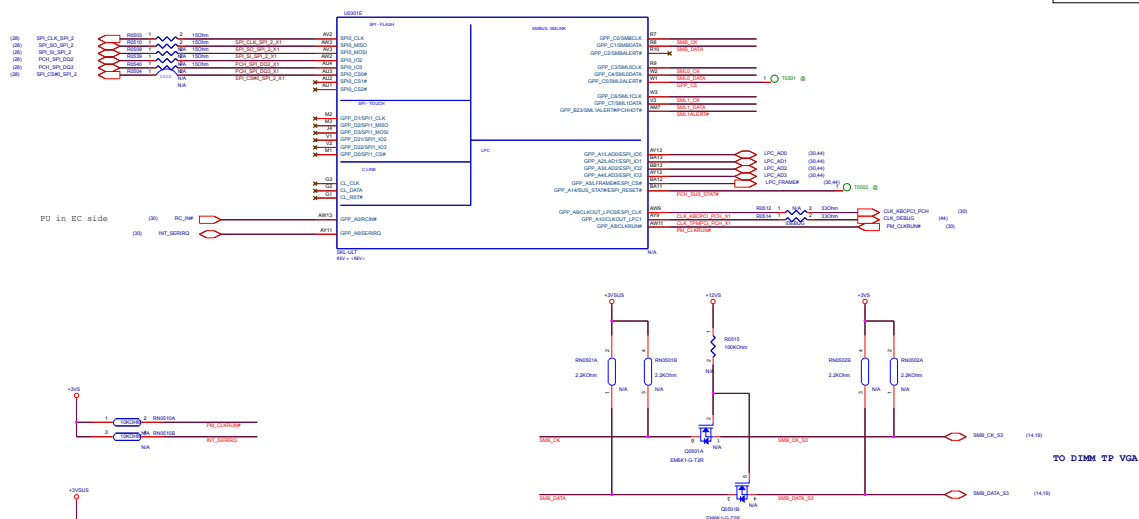
Toggle FullScreen

Pre Page

Next Page

MoveTo

Main Board



GPP_C5: weak internal pull down	
NO	EXP NOX
NO	LPC is selected for RC (Default)

Transport Layer Security (TLS) Confidentiality	
GFP_C2: weak internal pull down	
NO	Enable
NO	Disable Intel SGX TLS cipher suite (no confidentiality) (Default)

Schematic Page List

Hide

009_CPU_CFG,RSVD
010_CPU_POWER_CAP
011_
012_
013_DDR4_
014_DDR4_SO-DIMM_A
015_DDR4_
016_DDR4_SO-DIMM_B
017_DDR4_****
018_DDR4_****
019_DDR4_CA_DQ_VOLTAGE
020_CPU_PCH_CSI2,EMMC
021_CPU_PCH_CGPIO, LPIO, MI
SC
022_CPU_PCH_AUDIO,SDIO,SD
XC
023_CPU_PCH_PCIE,USB,SATA
024_CPU_PCH_CLOCK SIGNAL
S,RTC
025_CPU_PCH_SYS_POWER
026_CPU_PCH_POEWR,GND
027_CPU_PCH_POEWR,GND

Global Search

001

002

003

004

005

006

008

009

010

014

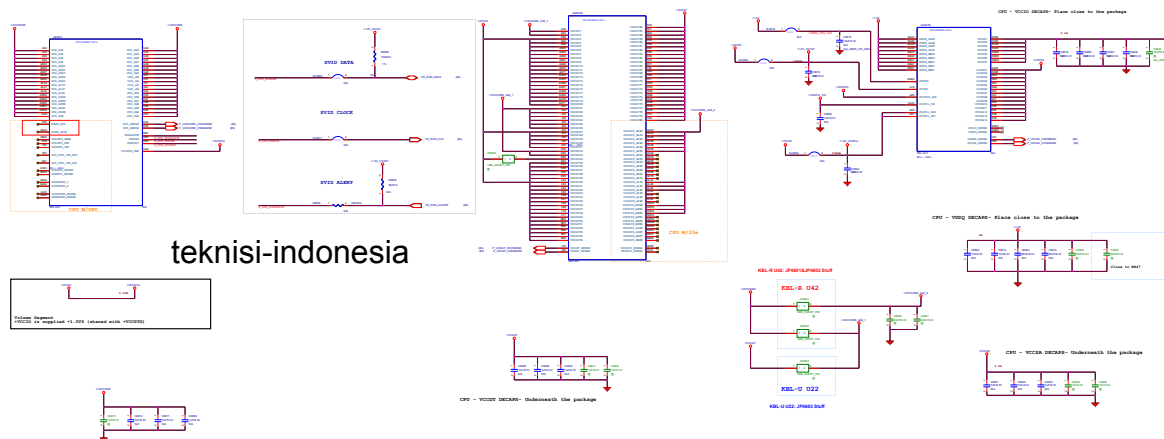
016

Toggle FullScreen

Pre Page

Next Page

MoveTo



Hide

009_CPU_CFG,RSVD
010_CPU_POWER_CAP
011_
012_
013_DDR4_
014_DDR4_SO-DIMM_A
015_DDR4_
016_DDR4_SO-DIMM_B
017_DDR4_****
018_DDR4_****
019_DDR4_CA_DQ_VOLTAGE
020_CPU_PCH_CSI2,EMMC
021_CPU_PCH_CGPIO, LPIO, MI
SC
022_CPU_PCH_AUDIO,SDIO,SD
XC
023_CPU_PCH_PCIE,USB,SATA
024_CPU_PCH_CLOCK SIGNAL
S,RTC
025_CPU_PCH_SYS_POWER
026_CPU_PCH_POEWR,GND
027_CPU_PCH_POEWR,GND

001

002

003

004

005

006

008

009

010

014

016

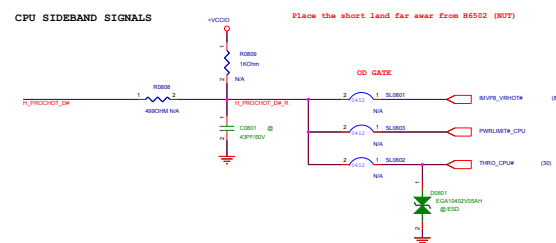
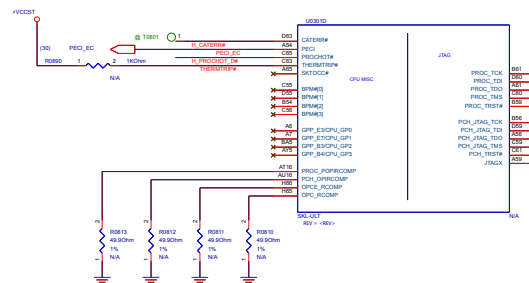
Toggle FullScreen

Pre Page

Next Page

MoveTo

Main Board



Hide

009_CPU_CFG,RSVD
010_CPU_POWER_CAP
011_
012_
013_DDR4_
014_DDR4_SO-DIMM_A
015_DDR4_
016_DDR4_SO-DIMM_B
017_DDR4_****
018_DDR4_****
019_DDR4_CA_DQ_VOLTAGE
020_CPU_PCH_CSI2,EMMC
021_CPU_PCH_CGPIO, LPIO, MI
SC
022_CPU_PCH_AUDIO,SDIO,SD
XC
023_CPU_PCH_PCIE,USB,SATA
024_CPU_PCH_CLOCK SIGNAL
S,RTC
025_CPU_PCH_SYS_POWER
026_CPU_PCH_POEWR,GND
027_CPU_PCH_POEWR,GND

Global Search

001

002

003

004

005

006

008

009

010

014

016

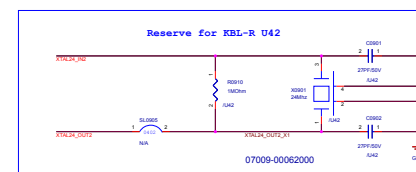
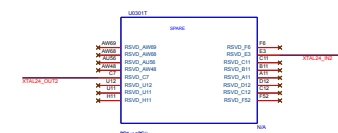
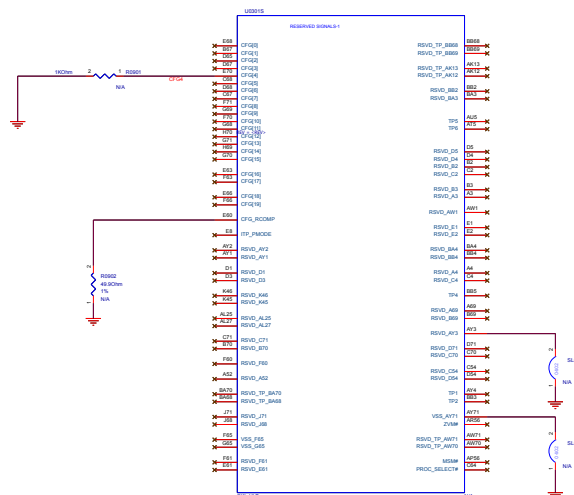
Toggle FullScreen

Pre Page

Next Page

MoveTo

Main Board



	1	0	NOTE
CFG0	NO STALL	STALL	STALL RESET SEQUENCE AFTER PCU PILL LOCK UNTIL DE-ASSERTED
CFG4	DISABLE	ENABLE	WOP ENABLE

Schematic Page List

Hide

009_CPU_CFG,RSVD

010_CPU_POWER_CAP

011_

012_

013_DDR4_

014_DDR4_SO-DIMM_A

015_DDR4_

016_DDR4_SO-DIMM_B

017_DDR4_****

018_DDR4_****

019_DDR4_CA_DQ_VOLTAGE

020_CPU_PCH_CSI2,EMMC

021_CPU_PCH_CGPIO, LPIO, MI
SC

022_CPU_PCH_AUDIO,SDIO,SD
XC

023_CPU_PCH_PCIE,USB,SATA

024_CPU_PCH_CLOCK SIGNAL
S,RTC

025_CPU_PCH_SYS_POWER

026_CPU_PCH_POEWR,GND

027_CPU_PCH_POEWR,GND

Global Search

001

002

003

004

005

006

008

009

010

014

016

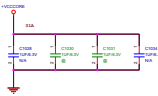
Toggle FullScreen

Pre Page

Next Page

MoveTo

CPU - VCC DECAPS- Underneath the package



CPU - VCC DECAPS- Place close to the package

ASUS

Title : CPU_POWER_CAP

Engineer: EE

Part: X705

Rev: 1.0

Schematic Page List

Hide

009_CPU_CFG,RSVD
010_CPU_POWER_CAP
011_
012_
013_DDR4_
014_DDR4_SO-DIMM_A
015_DDR4_
016_DDR4_SO-DIMM_B
017_DDR4_****
018_DDR4_****
019_DDR4_CA_DQ_VOLTAGE
020_CPU_PCH_CSI2,EMMC
021_CPU_PCH_CGPIO, LPIO, MI
SC
022_CPU_PCH_AUDIO,SDIO,SD
XC
023_CPU_PCH_PCIE,USB,SATA
024_CPU_PCH_CLOCK SIGNAL
S,RTC
025_CPU_PCH_SYS_POWER
026_CPU_PCH_POEWR,GND
027_CPU_PCH_POEWR,GND

Global Search

001

002

003

004

005

006

008

009

010

014

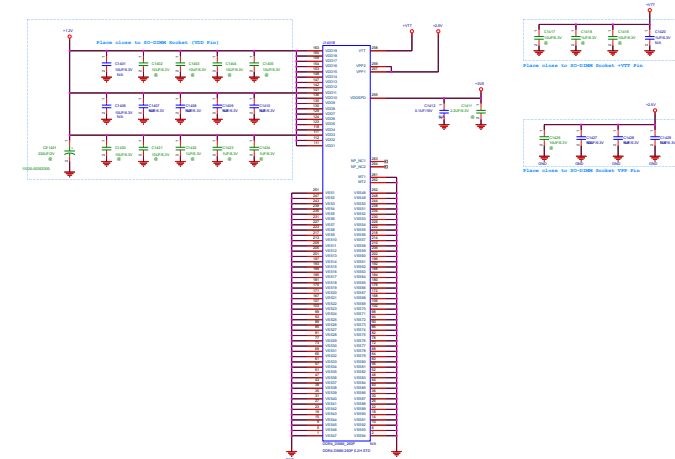
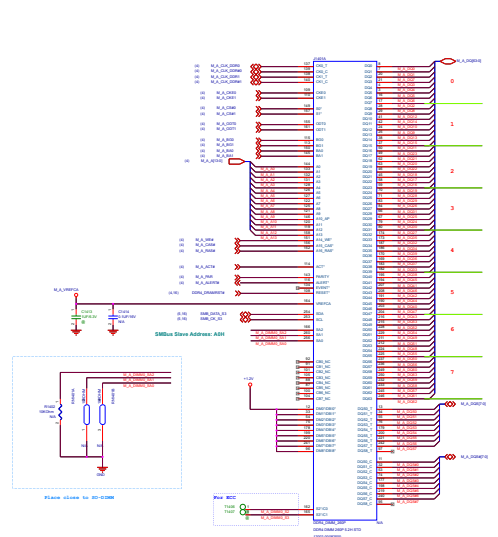
016

Toggle FullScreen

Pre Page

Next Page

MoveTo

www.teknisi-indonesia.com

Schematic Page List

Hide

009_CPU_CFG,RSVD
010_CPU_POWER_CAP
011_
012_
013_DDR4_
014_DDR4_SO-DIMM_A
015_DDR4_
016_DDR4_SO-DIMM_B
017_DDR4_****
018_DDR4_****
019_DDR4_CA_DQ_VOLTAGE
020_CPU_PCH_CSI2,EMMC
021_CPU_PCH_CGPIO, LPIO, MI
SC
022_CPU_PCH_AUDIO,SDIO,SD
XC
023_CPU_PCH_PCIE,USB,SATA
024_CPU_PCH_CLOCK SIGNAL
S,RTC
025_CPU_PCH_SYS_POWER
026_CPU_PCH_POEWR,GND
027_CPU_PCH_POEWR,GND

Global Search

001

002

003

004

005

006

008

009

010

014

016

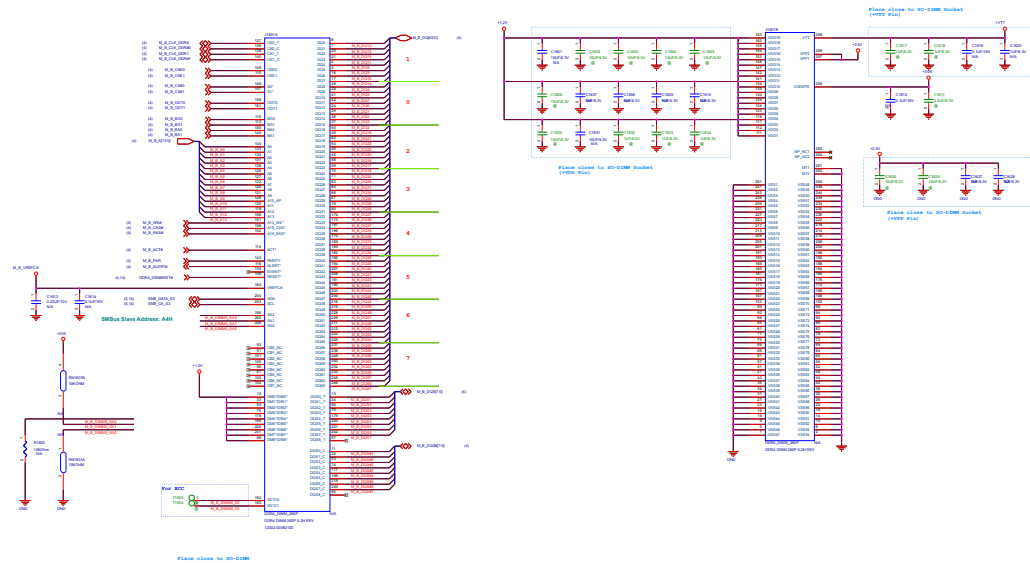
Toggle FullScreen

Pre Page

Next Page

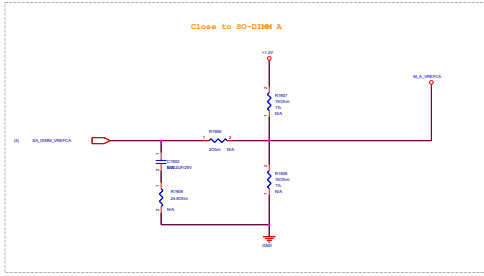
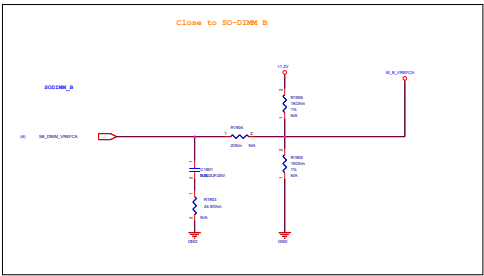
MoveTo

SODIMM CHB-DIMM0



Schematic Page List	Hide
009_CPU_CFG,RSVD	
010_CPU_POWER_CAP	
011_	
012_	
013_DDR4_	
014_DDR4 SO-DIMM_A	
015_DDR4_	
016_DDR4_SO-DIMM_B	
017_DDR4_****	
018_DDR4_****	
019_DDR4_CA_DQ_VOLTAGE	
020_CPU_PCH_CSI2,EMMC	
021_CPU_PCH_CGPIO, LPIO, MI SC	
022_CPU_PCH_AUDIO,SDIO,SD XC	
023_CPU_PCH_PCIE,USB,SATA	
024_CPU_PCH_CLOCK SIGNAL S,RTC	
025_CPU_PCH_SYS_POWER	
026_CPU_PCH_POEWR,GND	
027_CPU_PCH_POEWR,GND	

Global Search	019	020	021	022	023	
					Toggle FullScreen	Pre Page
						Next Page
						MoveTo



Hide

009_CPU_CFG,RSVD
010_CPU_POWER_CAP
011_
012_
013_DDR4_
014_DDR4_SO-DIMM_A
015_DDR4_
016_DDR4_SO-DIMM_B
017_DDR4_****
018_DDR4_****
019_DDR4_CA_DQ_VOLTAGE
020_CPU_PCH_CSI2,EMMC
021_CPU_PCH_CGPIO, LPIO, MI
SC
022_CPU_PCH_AUDIO,SDIO,SD
XC
023_CPU_PCH_PCIE,USB,SATA
024_CPU_PCH_CLOCK SIGNAL
S,RTC
025_CPU_PCH_SYS_POWER
026_CPU_PCH_POEWR,GND
027_CPU_PCH_POEWR,GND

Global Search

019

020

021

022

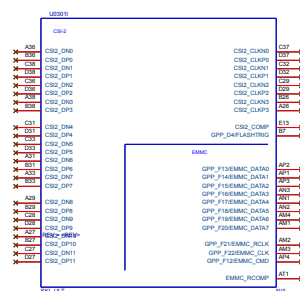
023

Toggle FullScreen

Pre Page

Next Page

MoveTo



Schematic Page List

Hide

009_CPU_CFG,RSVD
010_CPU_POWER_CAP
011_
012_
013_DDR4_
014_DDR4_SO-DIMM_A
015_DDR4_
016_DDR4_SO-DIMM_B
017_DDR4_****
018_DDR4_****
019_DDR4_CA_DQ_VOLTAGE
020_CPU_PCH_CSI2,EMMC
021_CPU_PCH_CGPIO, LPIO, MI
SC
022_CPU_PCH_AUDIO,SDIO,SD
XC
023_CPU_PCH_PCIE,USB,SATA
024_CPU_PCH_CLOCK SIGNAL
S,RTC
025_CPU_PCH_SYS_POWER
026_CPU_PCH_POEWR,GND
027_CPU_PCH_POEWR,GND

Global Search

019

020

021

022

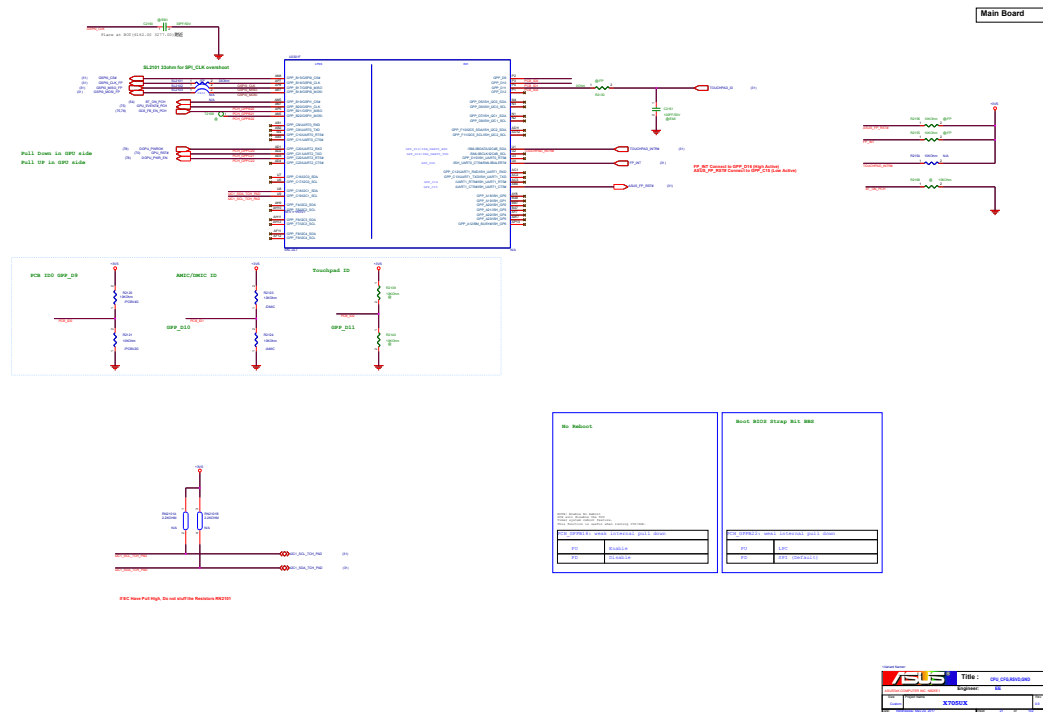
023

Toggle FullScreen

Pre Page

Next Page

MoveTo



Schematic Page List

Hide

009_CPU_CFG,RSVD
010_CPU_POWER_CAP
011_
012_
013_DDR4_
014_DDR4_SO-DIMM_A
015_DDR4_
016_DDR4_SO-DIMM_B
017_DDR4_****
018_DDR4_****
019_DDR4_CA_DQ_VOLTAGE
020_CPU_PCH_CSI2,EMMC
021_CPU_PCH_CGPIO, LPIO, MI
SC
022_CPU_PCH_AUDIO,SDIO,SD
XC
023_CPU_PCH_PCIE,USB,SATA
024_CPU_PCH_CLOCK SIGNAL
S,RTC
025_CPU_PCH_SYS_POWER
026_CPU_PCH_POEWR,GND
027_CPU_PCH_POEWR,GND

Global Search

019

020

021

022

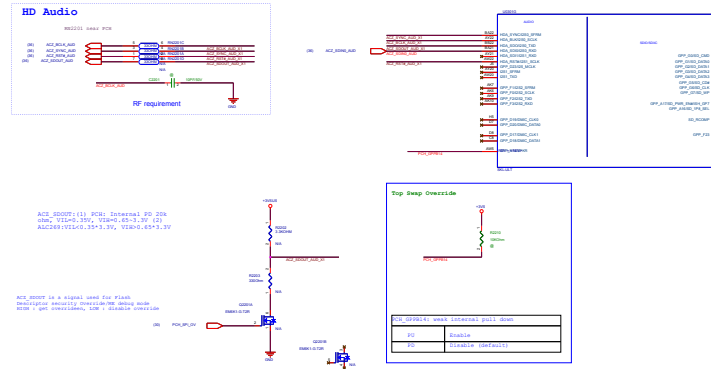
023

Toggle FullScreen

Pre Page

Next Page

MoveTo



Schematic Page List

Hide

009_CPU_CFG,RSVD
010_CPU_POWER_CAP
011_
012_
013_DDR4_
014_DDR4_SO-DIMM_A
015_DDR4_
016_DDR4_SO-DIMM_B
017_DDR4_****
018_DDR4_****
019_DDR4_CA_DQ_VOLTAGE
020_CPU_PCH_CSI2,EMMC
021_CPU_PCH_CGPIO, LPIO, MI
SC
022_CPU_PCH_AUDIO,SDIO,SD
XC
023_CPU_PCH_PCIE,USB,SATA
024_CPU_PCH_CLOCK SIGNAL
S,RTC
025_CPU_PCH_SYS_POWER
026_CPU_PCH_POEWR,GND
027_CPU_PCH_POEWR,GND

Global Search

019

020

021

022

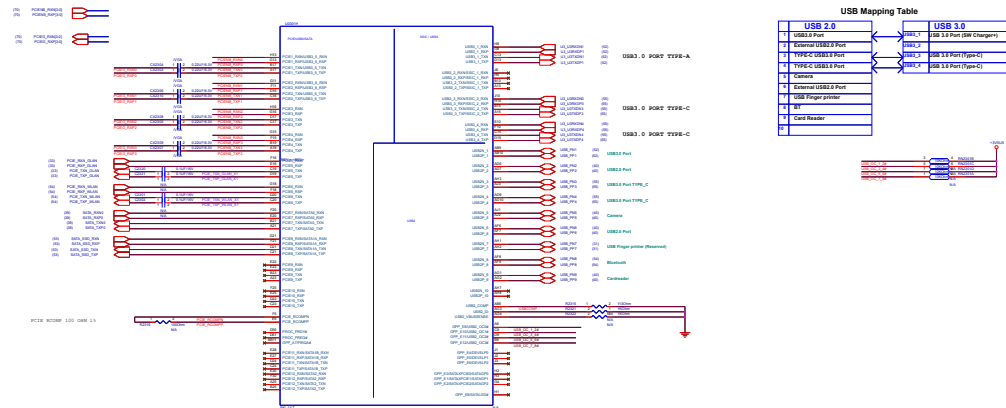
023

Toggle FullScreen

Pre Page

Next Page

MoveTo



Schematic Page List

Hide

022_CPU_PCH_AUDIO,SDIO,SD
XC

023_CPU_PCH_PCIE,USB,SATA

024_CPU_PCH_CLOCK SIGNAL
S,RTC

025_CPU_PCH_SYS_POWER

026_CPU_PCH_POEWR,GND

027_CPU_PCH_POEWR,GND

028_PCH-SPI ROM,OTH

029_PCH-XDP

030_KBC_ITE8995

031_KBC_KB & TP & FP

032_RST_Reset Circuit

033_LAN-RTL8111HS-CG

034_LAN_RJ45

035_

036-AUDIO_CODEC

037_

038_AUD-SPEAKER

039_SATA_HDD_CONN

040_B to B Connector

041 IO Board USB LED FPC Sc

Global Search

024

025

026

027

028

030

031

032

033

034

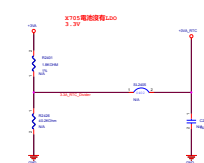
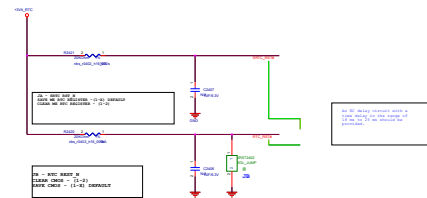
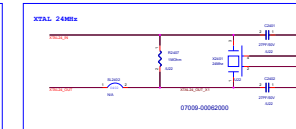
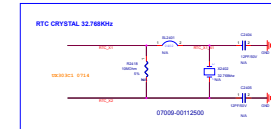
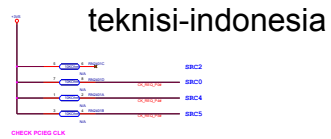
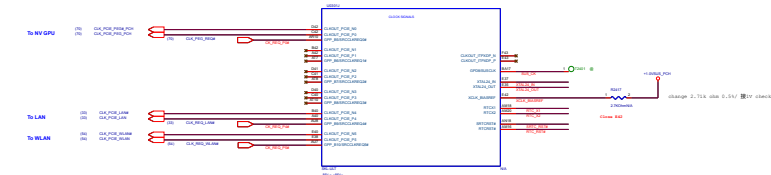
036

Toggle FullScreen

Pre Page

Next Page

MoveTo



Schematic Page List

Hide

022_CPU_PCH_AUDIO,SDIO,SD XC

023_CPU_PCH_PCIE,USB,SATA

024_CPU_PCH_CLOCK SIGNAL S,RTC

025_CPU_PCH_SYS_POWER

026_CPU_PCH_POEWR,GND

027_CPU_PCH_POEWR,GND

028_PCH-SPI ROM,OTH

029_PCH-XDP

030_KBC_ITE8995

031_KBC_KB & TP & FP

032_RST_Reset Circuit

033_LAN-RTL8111HS-CG

034_LAN_RJ45

035_

036-AUDIO_CODEC

037_

038_AUD-SPEAKER

039_SATA_HDD_CONN

040_B to B Connector

041 IO Board USB LED FPC Sc

Global Search

024

025

026

027

028

030

031

032

033

034

036

Toggle FullScreen

Pre Page

Next Page

MoveTo

Main Board

Schematic Page List

Hide

022_CPU_PCH_AUDIO,SDIO,SD
XC
023_CPU_PCH_PCIE,USB,SATA
024_CPU_PCH_CLOCK SIGNAL
S,RTC
025_CPU_PCH_SYS_POWER
026_CPU_PCH_POEWR,GND
027_CPU_PCH_POEWR,GND
028_PCH-SPI ROM,OTH
029_PCH-XDP
030_KBC_ITE8995
031_KBC_KB & TP & FP
032_RST_Reset Circuit
033_LAN-RTL8111HS-CG
034_LAN_RJ45
035_
036-AUDIO_CODEC
037_
038_AUD-SPEAKER
039_SATA_HDD_CONN
040_B to B Connector
041 IO Board USB LED FPC Sc

Global Search

024

025

026

027

028

030

031

032

033

034

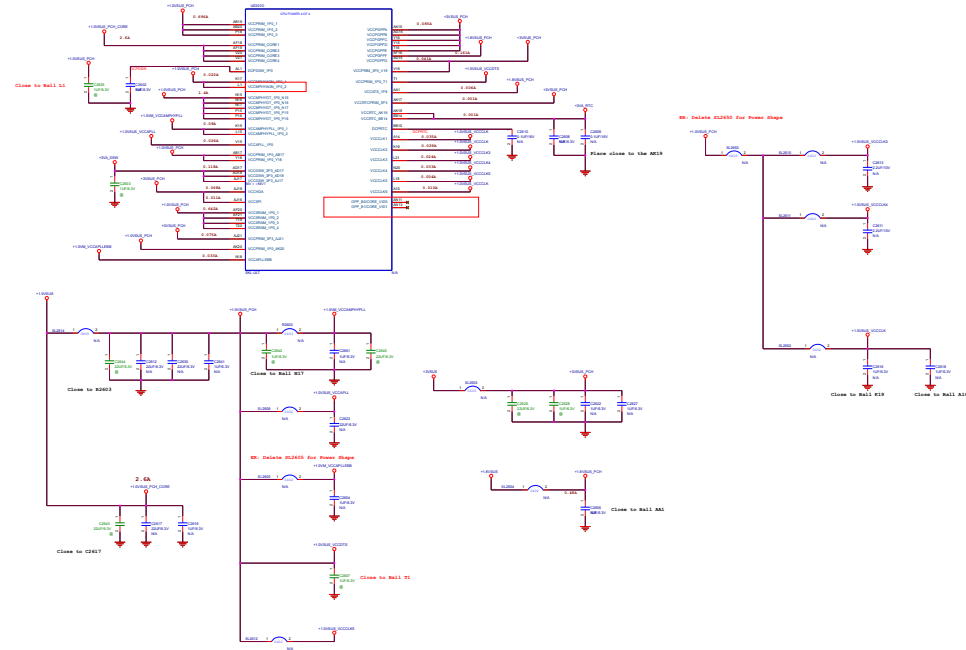
036

Toggle FullScreen

Pre Page

Next Page

MoveTo



Schematic Page List

Hide

022_CPU_PCH_AUDIO,SDIO,SD
XC

023_CPU_PCH_PCIE,USB,SATA

024_CPU_PCH_CLOCK SIGNAL
S,RTC

025_CPU_PCH_SYS_POWER

026_CPU_PCH_POEWR,GND

027_CPU_PCH_POEWR,GND

028_PCH-SPI ROM,OTH

029_PCH-XDP

030_KBC_ITE8995

031_KBC_KB & TP & FP

032_RST_Reset Circuit

033_LAN-RTL8111HS-CG

034_LAN_RJ45

035_

036-AUDIO_CODEC

037_

038_AUD-SPEAKER

039_SATA_HDD_CONN

040_B to B Connector

041_IO Board USB LED FPC Sc

Global Search

024

025

026

027

028

030

031

032

033

034

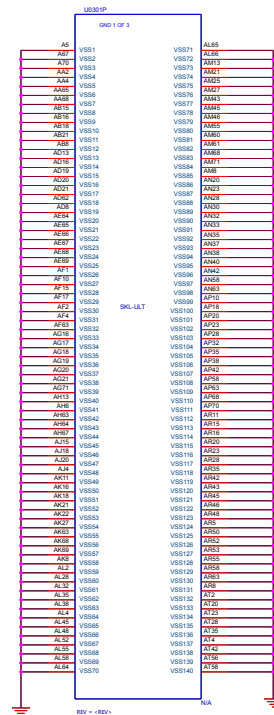
036

Toggle FullScreen

Pre Page

Next Page

MoveTo



Schematic Page List

Hide

022_CPU_PCH_AUDIO,SDIO,SD XC

023_CPU_PCH_PCIE,USB,SATA

024_CPU_PCH_CLOCK SIGNAL S,RTC

025_CPU_PCH_SYS_POWER

026_CPU_PCH_POEWR,GND

027_CPU_PCH_POEWR,GND

028_PCH-SPI ROM,OTH

029_PCH-XDP

030_KBC_ITE8995

031_KBC_KB & TP & FP

032_RST_Reset Circuit

033_LAN-RTL8111HS-CG

034_LAN_RJ45

035_

036-AUDIO_CODEC

037_

038_AUD-SPEAKER

039_SATA_HDD_CONN

040_B to B Connector

041 IO Board USB LED FPC Sc

Global Search

024

025

026

027

028

030

031

032

033

034

036

Toggle FullScreen

Pre Page

Next Page

MoveTo

024_CPU_PCH_CLOCK SIGNAL S,RTC

025_CPU_PCH_SYS_POWER

026_CPU_PCH_POEWR,GND

027_CPU_PCH_POEWR,GND

028_PCH-SPI ROM,OTH

029_PCH-XDP

030_KBC_ITE8995

031_KBC_KB & TP & FP

032_RST_Reset Circuit

033_LAN-RTL8111HS-CG

034_LAN_RJ45

035_

036-AUDIO_CODEC

037_

038_AUD-SPEAKER

039_SATA_HDD_CONN

040_B to B Connector

041 IO Board USB LED FPC Sc

SPI PCH Power

Thermal Sensor

System Management Interface

ASUS

Title : PCH-SPN ROM,OTH

Engineer: EE

X7000UX

Schematic Page List

Hide

```
022_CPU_PCH_AUDIO,SDIO,SD
XC
```

023_CPU_PCH_PCIE,USB,SATA

024_CPU_PCH_CLOCK SIGNAL
S,RTC

025_CPU_PCH_SYS_POWER

026_CPU_PCH_POEWR,GND

027_CPU_PCH_POEWR,GND

028_PCH-SPI ROM,OTH

029_PCH-XDP

030_KBC_ITE8995

031_KBC_KB & TP & FP

032_RST_Reset Circuit

033_LAN-RTL8111HS-CG

034_LAN_RJ45

035_

036-AUDIO_CODEC

037_

038_AUD-SPEAKER

039_SATA_HDD_CONN

040_B to B Connector

041 IO Board USB LED FPC Sc

Global Search

024

025

026

027

028

030

031

032

033

034

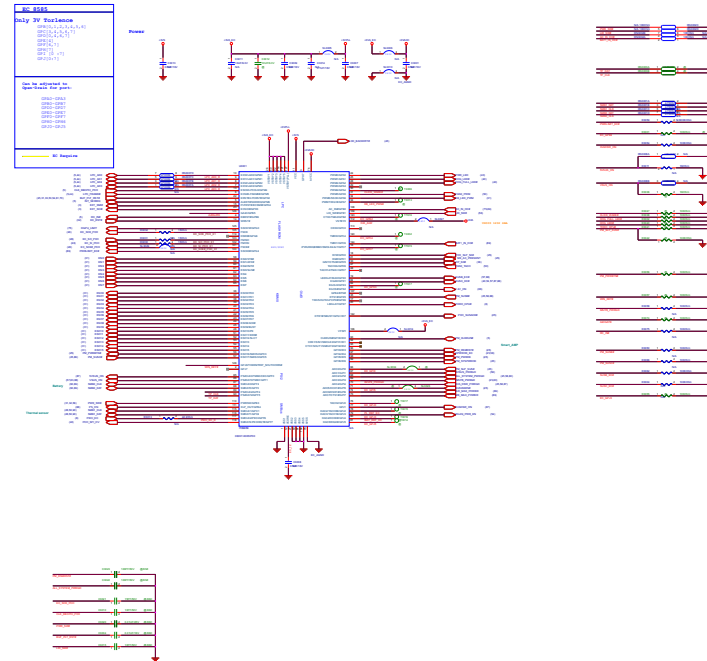
036

Toggle FullScreen

Pre Page

Next Page

MoveTo



Schematic Page List

Hide

022_CPU_PCH_AUDIO,SDIO,SD
XC
023_CPU_PCH_PCIE,USB,SATA
024_CPU_PCH_CLOCK SIGNAL
S,RTC
025_CPU_PCH_SYS_POWER
026_CPU_PCH_POEWR,GND
027_CPU_PCH_POEWR,GND
028_PCH-SPI ROM,OTH
029_PCH-XDP
030_KBC_ITE8995
031_KBC_KB & TP & FP
032_RST_Reset Circuit
033_LAN-RTL8111HS-CG
034_LAN_RJ45
035_
036-AUDIO_CODEC
037_
038_AUD-SPEAKER
039_SATA_HDD_CONN
040_B to B Connector
041 IO Board USB LED FPC Sc

Global Search

024

025

026

027

028

030

031

032

033

034

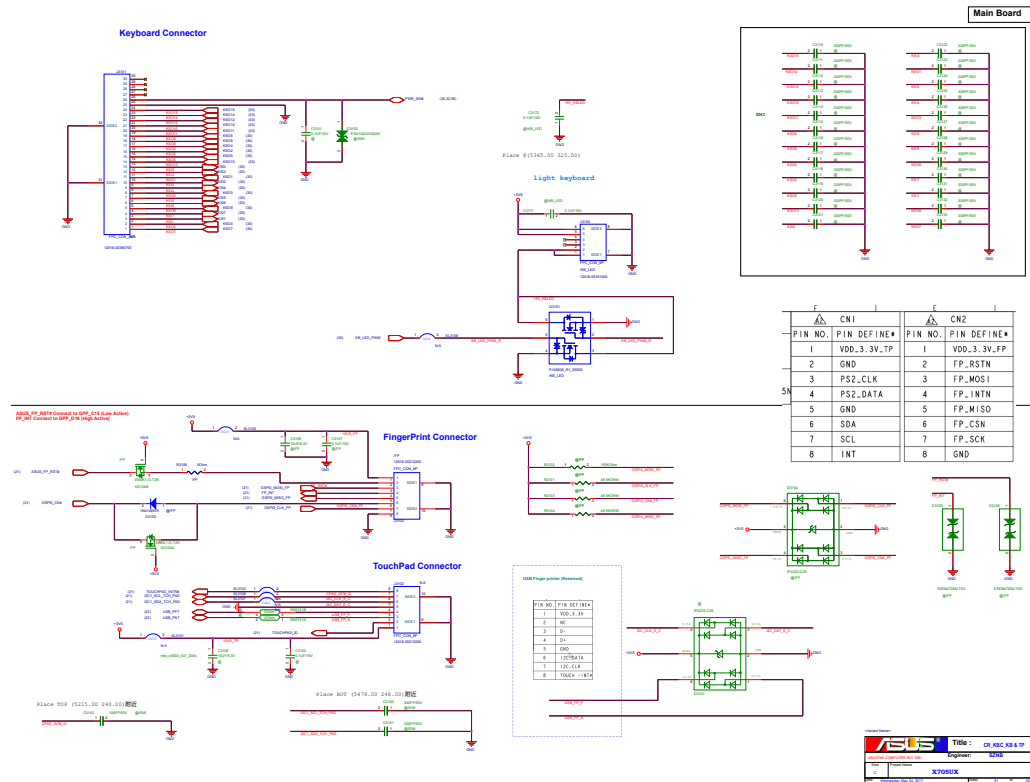
036

Toggle FullScreen

Pre Page

Next Page

MoveTo

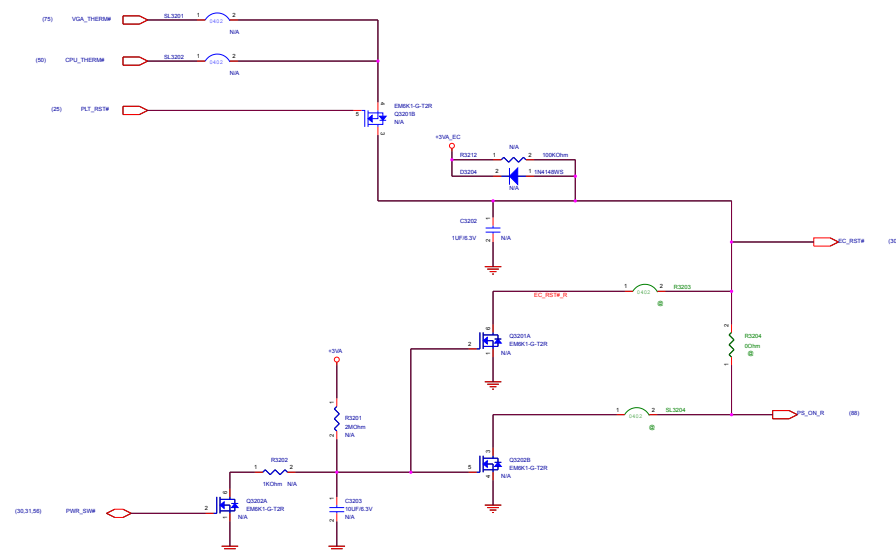


Hide

041 IO Board USB LED FPC Sc

036

MoveTo



Schematic Page List

Hide

022_CPU_PCH_AUDIO,SDIO,SD
XC

023_CPU_PCH_PCIE,USB,SATA

024_CPU_PCH_CLOCK SIGNAL
S,RTC

025_CPU_PCH_SYS_POWER

026_CPU_PCH_POEWR,GND

027_CPU_PCH_POEWR,GND

028_PCH-SPI ROM,OTH

029_PCH-XDP

030_KBC_ITE8995

031_KBC_KB & TP & FP

032_RST_Reset Circuit

033_LAN-RTL8111HS-CG

034_LAN_RJ45

035_

036-AUDIO_CODEC

037_

038_AUD-SPEAKER

039_SATA_HDD_CONN

040 B to B Connector

041 IO Board USB LED FPC Sc

Global Search

024

025

026

027

028

030

031

032

033

034

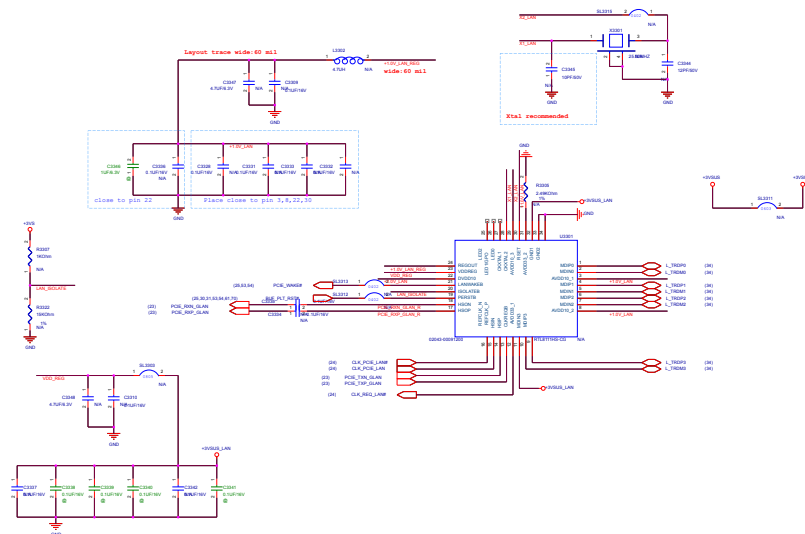
036

Toggle FullScreen

Pre Page

Next Page

MoveTo



Schematic Page List

Hide

022_CPU_PCH_AUDIO,SDIO,SD
XC

023_CPU_PCH_PCIE,USB,SATA

024_CPU_PCH_CLOCK SIGNAL
S,RTC

025_CPU_PCH_SYS_POWER

026_CPU_PCH_POEWR,GND

027_CPU_PCH_POEWR,GND

028_PCH-SPI ROM,OTH

029_PCH-XDP

030_KBC_ITE8995

031_KBC_KB & TP & FP

032_RST_Reset Circuit

033_LAN-RTL8111HS-CG

034_LAN_RJ45

035_

036-AUDIO_CODEC

037_

038_AUD-SPEAKER

039_SATA_HDD_CONN

040_B to B Connector

041_IO Board USB LED FPC Sc

Global Search

024

025

026

027

028

030

031

032

033

034

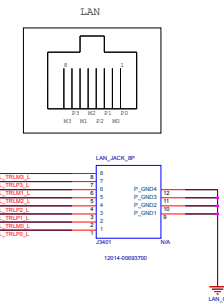
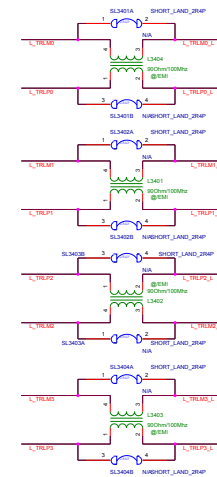
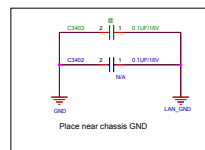
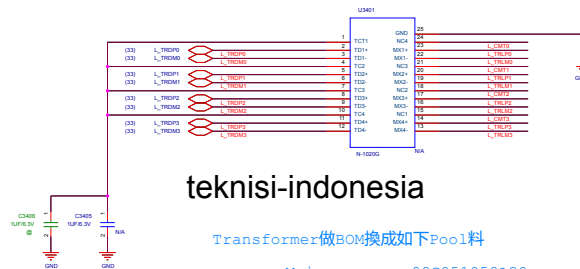
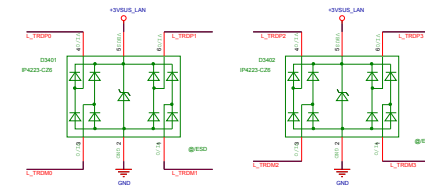
036

Toggle FullScreen

Pre Page

Next Page

MoveTo



Schematic Page List

Hide

022_CPU_PCH_AUDIO,SDIO,SD
XC

023_CPU_PCH_PCIE,USB,SATA

024_CPU_PCH_CLOCK SIGNAL
S,RTC

025 CPU PCH SYS POWER

026_CPU_PCH_POEWR,GND

027_CPU_PCH_POEWR,GND

028 PCH-SPI ROM,OTH

029 PCH-XDP

030_KBC_ITE8995

031_KBC_KB & TP & FP

032_RST_Reset Circuit

033 LAN-RTL8111HS-CG

034_LAN_RJ45

035_

036-AUDIO_CODEC

037_

038_AUD-SPEAKER

039_SATA_HDD_CONN

040_B to B Connector

041 IO Board USB LED FPC Sc

Global Search

024

025

026

027

028

030

031

032

033

034

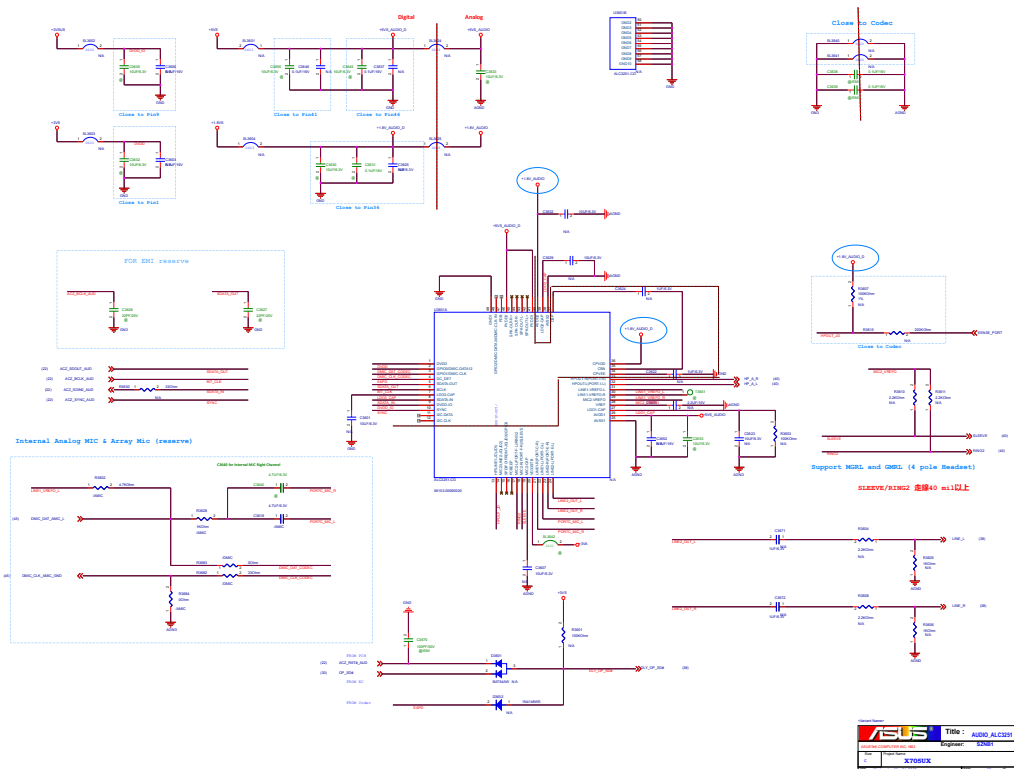
036

Toggle FullScreen

Pre Page

Next Page

MoveTo



Schematic Page List

Hide

Global Search

038

039

040

044

045

048

050

052

053

054

055

Toggle FullScreen

Pre Page

Next Page

MoveTo

036-AUDIO_CODEC

037_

038_AUD-SPEAKER

039 SATA HDD CONN

040_B to B Connector

041_IO Board_USB_LED_FPC_Sc
rew

042_IO Board_CR_AU6465R

043_

044_DEBUG_LPC

045_LCD_eDP_CMOS_DMIC

046_

047_

048_HDMI-Type-D

049_*****

050_Thermal Sensor & Fan

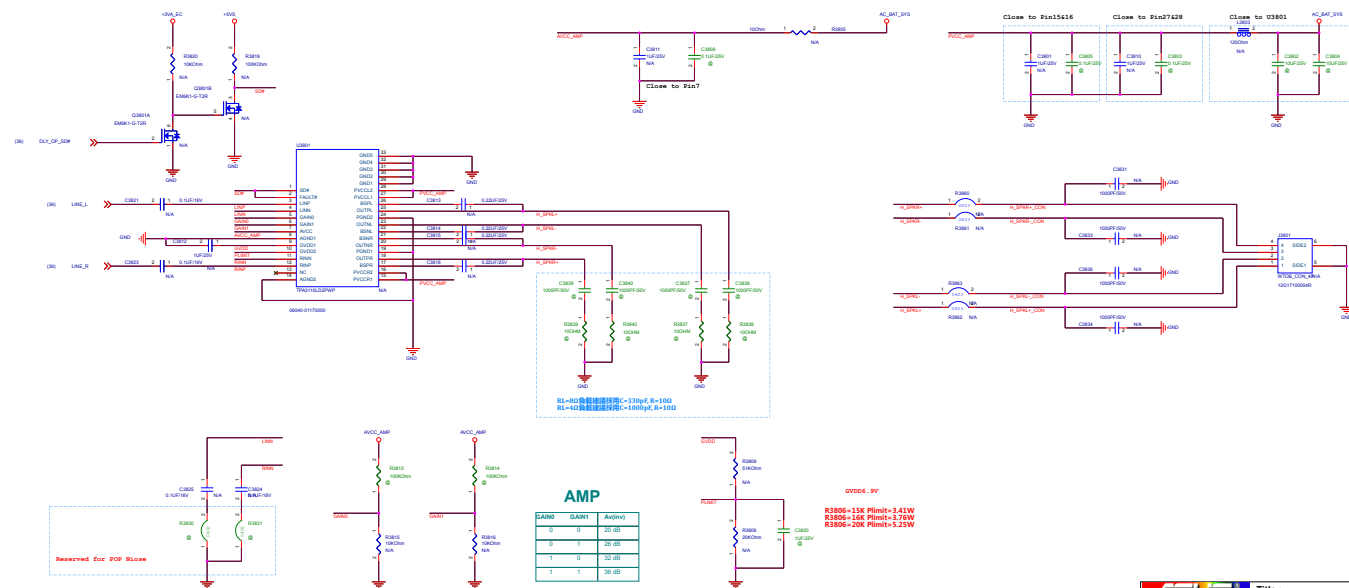
051

052_USB3.0 PORT

053. NGFF_SSD_CONN

054_NGFF_WLAN_BT


055_USB3.0_TYPEC



GAIN0	GAIN1	$A_v(\text{inv})$
0	0	20 dB
0	1	26 dB
1	0	32 dB
1	1	36 dB

GVDD06 = 9V

R3806=15K Plimit=3.41W
R3806=16K Plimit=3.76W
R3806=20K Plimit=5.25W

		Title : AUD-AMP&SPEAKER	
ASUSTek COMPUTER INC.		Engineer: SZNB1	
Size	Project Name		
Custom	X705UX		

Schematic Page List

Hide

036-AUDIO_CODEC

037_

038_AUD-SPEAKER

039_SATA_HDD_CONN

040_B to B Connector

041_IO Board_USB_LED_FPC_Sc
rew

042_IO Board_CR_AU6465R

043_

044_DEBUG_LPC

045_LCD_eDP_CMOS_DMIC

046_

047_

048_HDMI-Type-D

049_*****

050_Thermal Sensor & Fan

051_

052_USB3.0 PORT

053. NGFF_SSD_CONN

054_NGFF_WLAN_BT

055_USB3.0_TYPEC

Global Search

038

039

040

044

045

048

050

052

053

054

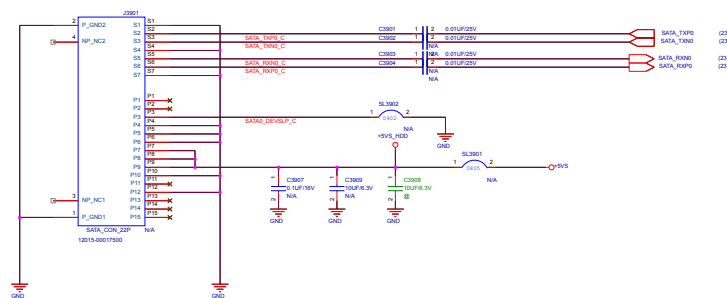
055

Toggle FullScreen

Pre Page

Next Page

MoveTo



Schematic Page List

Hide

036-AUDIO_CODEC

037_

038_AUD-SPEAKER

039_SATA_HDD_CONN

040_B to B Connector

041_IO Board_USB_LED_FPC_Screw

042_IO Board_CR_AU6465R

043_

044_DEBUG_LPC

045_LCD_eDP_CMOS_DMIC

046_

047_

048_HDMI-Type-D

049_*****

050_Thermal Sensor & Fan

051_

052_USB3.0 PORT

053. NGFF_SSD_CONN

054_NGFF_WLAN_BT

055_USB3.0_TYPEC

Global Search

038

039

040

044

045

048

050

052

053

054

055

Toggle FullScreen

Pre Page

Next Page

MoveTo

TO IO BD

www.teknisi-indonesia.com

ASUS

Model Name

T100

CE_A8802_0011_1

Part Number

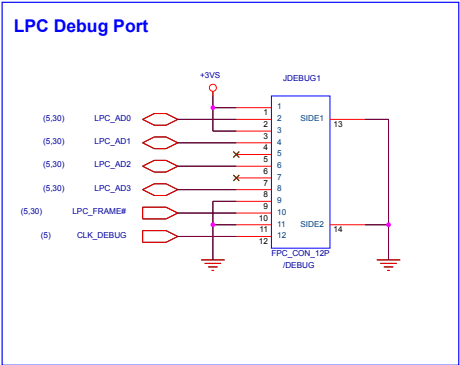
82002

Part Name

XS0010

Schematic Page List	Hide
036-AUDIO_CODEEC	
037_	
038_AUD-SPEAKER	
039_SATA_HDD_CONN	
040_B to B Connector	
041_IO Board_USB_LED_FPC_Sc rew	
042_IO Board_CR_AU6465R	
043_	
044_DEBUG_LPC	
045_LCD_eDP_CMOS_DMIC	
046_	
047_	
048_HDMI-Type-D	
049_*****	
050_Thermal Sensor & Fan	
051_	
052_USB3.0 PORT	
053. NGFF_SSD_CONN	
054_NGFF_WLAN_BT	
055_USB3.0_TYPEC	

Global Search	038	039	040	044	045	048	050	052	053	054	055
					Toggle FullScreen		Pre Page	Next Page	MoveTo		



<Variant Name>		
		Title : SB_DEBUG_LPC
ASUSTeK COMPUTER INC.		Engineer: SZNB2
Size	Project Name	Rev
A	X705UX	R1.0
Date: Wednesday, May 24, 2017	Sheet 44 of 102	

Schematic Page List

Hide

036-AUDIO_CODEC
037_
038_AUD-SPEAKER
039_SATA_HDD_CONN
040_B to B Connector
041_IO Board_USB_LED_FPC_Sc
rew
042_IO Board_CR_AU6465R
043_
044_DEBUG_LPC
045_LCD_eDP_CMOS_DMIC
046_
047_
048_HDMI-Type-D
049_*****
050_Thermal Sensor & Fan
051_
052_USB3.0 PORT
053_NGFF_SSD_CONN
054_NGFF_WLAN_BT
055_USB3.0_TYPEC

Global Search

038

039

040

044

045

048

050

052

053

054

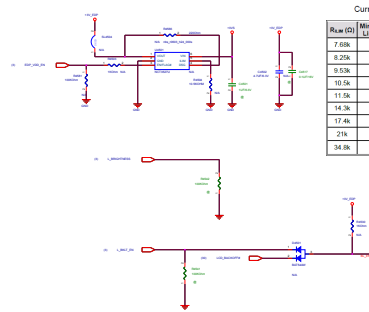
055

Toggle FullScreen

Pre Page

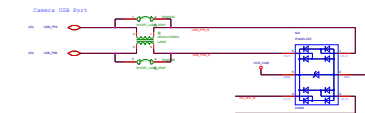
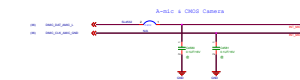
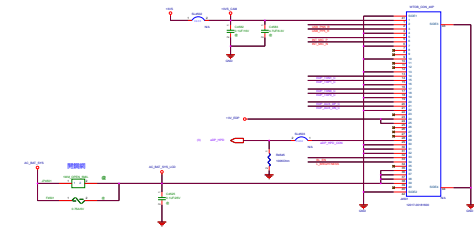
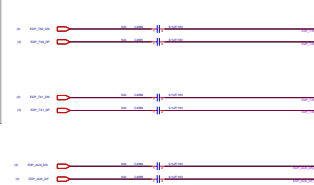
Next Page

MoveTo

Current Limit vs. R_{LIM} Values

R _{LIM} (Ω)	Min. Current Limit (mA)	Typ. Current Limit (mA)	Max. Current Limit (mA)
7.50k	2430	2100	2070
8.25k	2250	2000	2750
9.53k	1980	2200	2420
10.5k	1800	2000	2200
11.5k	1620	1800	1980
14.7k	1260	1500	1650
17.4k	1080	1200	1320
21k	900	1000	1100
34.8k	500	600	700

eDP Panel differential signals



Schematic Page List

Hide

036-AUDIO_CODEEC
037_
038_AUD-SPEAKER
039_SATA_HDD_CONN
040_B to B Connector
041_IO Board_USB_LED_FPC_Sc
rew
042_IO Board_CR_AU6465R
043_
044_DEBUG_LPC
045_LCD_eDP_CMOS_DMIC
046_
047_
048_HDMI-Type-D
049_*****
050_Thermal Sensor & Fan
051_
052_USB3.0 PORT
053_NGFF_SSD_CONN
054_NGFF_WLAN_BT
055_USB3.0_TYPEC

Global Search

038

039

040

044

045

048

050

052

053

054

055

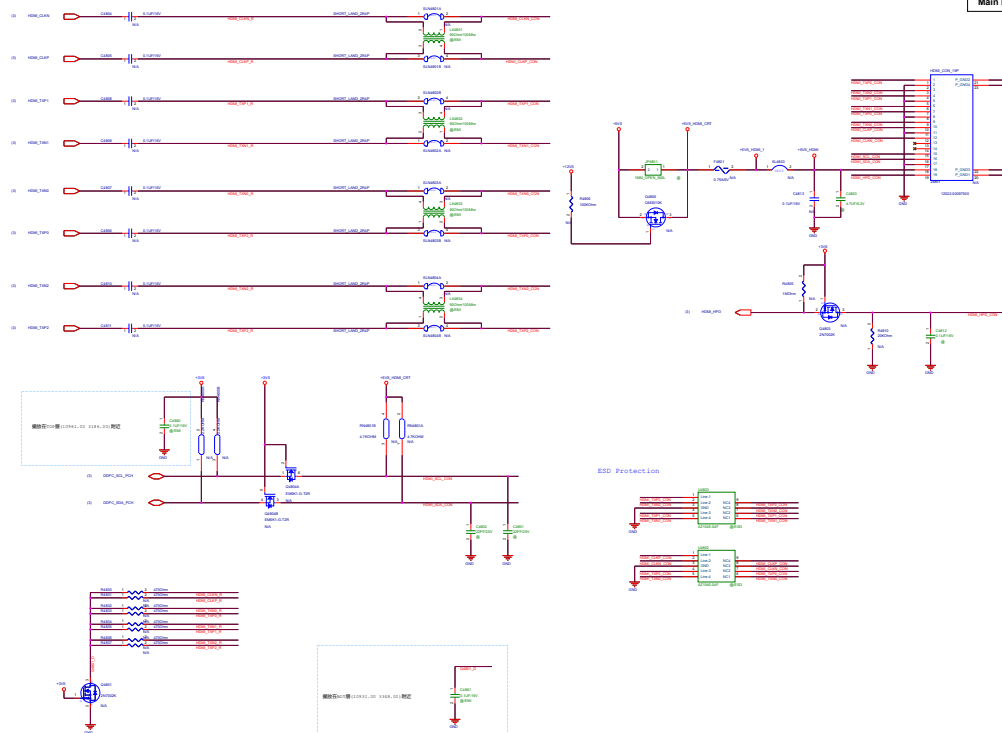
Toggle FullScreen

Pre Page

Next Page

MoveTo

Main Board



Schematic Page List

Hide

036-AUDIO_CODEEC

037_

038_AUD-SPEAKER

039_SATA_HDD_CONN

040_B to B Connector

041_IO Board_USB_LED_FPC_Screw

042_IO Board_CR_AU6465R

043_

044_DEBUG_LPC

045_LCD_eDP_CMOS_DMIC

046_

047_

048_HDMI-Type-D

049_*****

050_Thermal Sensor & Fan

051_

052_USB3.0 PORT

053. NGFF_SSD_CONN

054_NGFF_WLAN_BT

055_USB3.0_TYPEC

Global Search

038

039

040

044

045

048

050

052

053

054

055

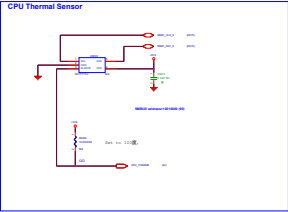
Toggle FullScreen

Pre Page

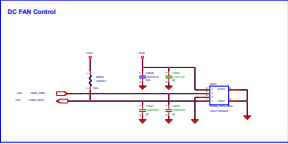
Next Page

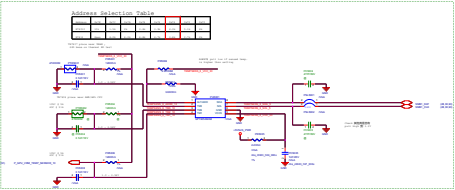
MoveTo


CPU Thermal Sensor



DC FAN Control







Schematic Page List

Hide

036-AUDIO_CODEEC

037_

038_AUD-SPEAKER

039_SATA_HDD_CONN

040_B to B Connector

041_IO Board_USB_LED_FPC_Sc
rew

042_IO Board_CR_AU6465R

043_

044_DEBUG_LPC

045_LCD_eDP_CMOS_DMIC

046_

047_

048_HDMI-Type-D

049_*****

050_Thermal Sensor & Fan

051_

052_USB3.0 PORT

053. NGFF_SSD_CONN

054_NGFF_WLAN_BT

055_USB3.0_TYPEC

Global Search

038

039

040

044

045

048

050

052

053

054

055

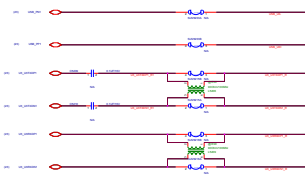
Toggle FullScreen

Pre Page

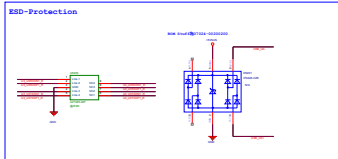
Next Page

MoveTo


USB3.0_Port 0





ESD-Protection



www.teknisi-indonesia.com

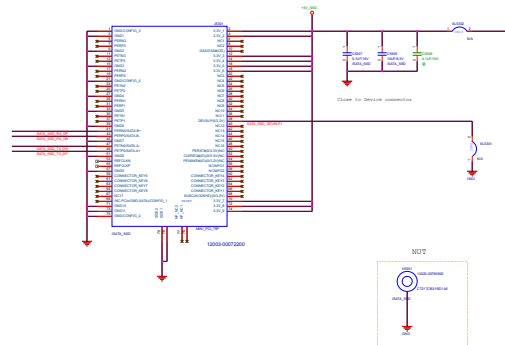
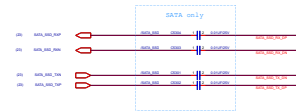






Schematic Page List	Hide
036-AUDIO_CODEC	
037_	
038_AUD-SPEAKER	
039_SATA_HDD_CONN	
040_B to B Connector	
041_IO Board_USB_LED_FPC_Sc rew	
042_IO Board_CR_AU6465R	
043_	
044_DEBUG_LPC	
045_LCD_eDP_CMOS_DMIC	
046_	
047_	
048_HDMI-Type-D	
049_*****	
050_Thermal Sensor & Fan	
051_	
052_USB3.0 PORT	
053. NGFF_SSD_CONN	
054_NGFF_WLAN_BT	
055_USB3.0_TYPEC	

Global Search	038	039	040	044	045	048	050	052	053	054	055
Toggle FullScreen							Pre Page	Next Page	MoveTo		



36.3.2.4 AC Capacitor General Guidelines for M.2 SSD Storage Routing on SATA / PCI Express* Multiplexed Ports

The following table summarizes the AC capacitor requirements on the motherboard when using the SATA/PCIe* multiplexed ports.

Note: When SATA and PCIe* are muxed, always route according to SATA design guidelines. SATA does not support signal polarity reversal and does not support lane reversal.

Table 36-7. SATA / PCI Express* Gen 2 and Gen 3 Capacitor Values

Condition	PCI Express* Gen 2 Only	PCI Express* Gen 3 Only	SATA Only	PCI Express* Gen 2 / SATA	PCI Express* Gen 3 / SATA
Processor Tx	100 nF	220 nF	10 nF	100 nF	220 nF
Processor Rx	None	None	10 nF ²	None	None ²

Footnote:

- Design Constraint: For PCIe only application, please refer to the PCIe guidelines for details.
- Design Constraint: For SATA only application, both Tx and Rx channels need to have 10 nF capacitors on the motherboard. This option supports all SATA devices. However, the 10 nF capacitor on Rx can be removed if DC coupled ODDs / Devices are NOT used.
- Design Constraint: For PCIe* Gen 2 / SATA multiplexed configuration, motherboard Tx requires a 100 nF AC capacitor and NO AC capacitor is required for motherboard Rx channel. **This option DOES NOT support DC coupled ODDs / Devices.**
- Design Constraint: For PCIe* Gen 3 / SATA multiplexed configuration, motherboard Tx requires a 220 nF AC capacitor and NO AC capacitor is required for motherboard Rx channel. **This option DOES NOT support DC coupled ODDs / Devices.**
- Design Constraint: Required Refer Chapter 3, "General Differential Signals Design Guidelines" along with the additional guidelines in this section for all design optimization guidelines.
- Design Constraint: For PCIe* lane that needs to support either PCIe* Gen2 devices or PCIe* Gen3 devices, follow the PCIe* Gen 3 / SATA multiplexed configuration where the motherboard Tx requires a 220 nF AC capacitor and NO AC capacitor is required for motherboard Rx channel. **This option DOES NOT support DC coupled ODDs / Devices.**

Schematic Page List	Hide
036-AUDIO_CODEEC	
037_	
038_AUD-SPEAKER	
039_SATA_HDD_CONN	
040_B to B Connector	
041_IO Board_USB_LED_FPC_Sc rew	
042_IO Board_CR_AU6465R	
043_	
044_DEBUG_LPC	
045_LCD_eDP_CMOS_DMIC	
046_	
047_	
048_HDMI-Type-D	
049_*****	
050_Thermal Sensor & Fan	
051_	
052_USB3.0 PORT	
053_NGFF_SSD_CONN	
054_NGFF_WLAN_BT	
055_USB3.0_TYPEC	

Global Search

038039040044045048050052053054055

Toggle FullScreenPre PageNext PageMoveTo

Schematic Page List

Hide

Global Search

038

039

040

044

045

048

050

052

053

054

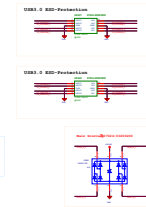
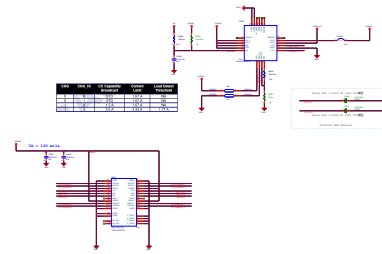
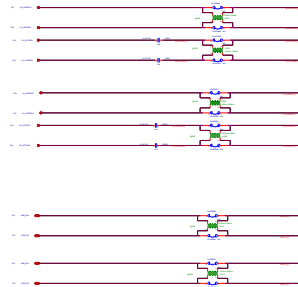
055

Toggle FullScreen

Pre Page

Next Page

MoveTo [



Schematic Page List

Hide

051_

052_USB3.0 PORT

053. NGFF_SSD_CONN

054_NGFF_WLAN_BT

055_USB3.0_TYPEC

056_Hall Sensor & Power Switch

057_DSG_Discharge

058_PRO_Protect

059_*****

060_PW_DC_DC & BAT IN

061_EMI_RESERVE

062_*****

063_*****

064_*****

065_SKEW_HOLE_SMT_NUT

066_*****

067_*****

068_

069_*****

070_GPU_PCI-EXPRESS

071_GPU_MEMORY INTERFACE

Global Search

056

Toggle FullScreen

Pre Page

Next Page

MoveTo

ASUS

Title : Hall Sensor & Power SW

Author : Engineer

Part : X7000X

Version : 1.0

File : 056

Schematic Page List

Hide

U68_
069_*****
070_GPU_PCI-EXPRESS
071_GPU_MEMORY INTERFACE
072_VGA_GDDR5_(Bit0~63)
073_VGA_GDDR5_(NA)
074_VGA_nVIDIA_N16V/S_DISPLAY
075_GPU_GPIO_THERM
076_GPU_XTAL/STRAPPING
077_GPU_PWG/GND
078_GPU_Power_Sel
079_*****
080_PW_KABY LAKE-U(1)
081_PW_KABY LAKE-U(2)
082_PW_***
083_PW_+1.0VSUS/1.8VSUS(UP
9002)
084_PW_
085_PW_***
086_PW_1.2V/+VTT/+2.5V(UP900
2)

Global Search

057

058

060

061

065

070

071

072

074

075

076

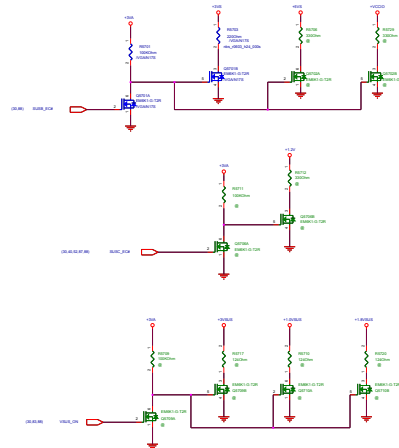
Toggle FullScreen

Pre Page

Next Page

MoveTo

Main Board

www.teknisi-indonesia.com

Schematic Page List

Hide

068

069_*****

070_GPU_PCI-EXPRESS

071_GPU_MEMORY INTERFACE

072_VGA_GDDR5_(Bit0~63)

073_VGA_GDDR5_(NA)

074_VGA_nVIDIA_N16V/S_DISPL
AY

075_GPU_GPIO_THERM

076 GPU XTAL/STRAPPING

077_GPU_PWG/GND

078_GPU_Power_Sel

079_*****

080_PW_KABY LAKE-U(1)

081_PW_KABY LAKE-U(2)

082_PW_***

083_PW_+1.0VSUS/1.8VSUS(UP
9002)

084_PW_

085_PW_***

086_PW_1.2V/+VTT/+2.5V(UP900
2)

Global Search

057

058

060

061

065

070

071

072

074

075

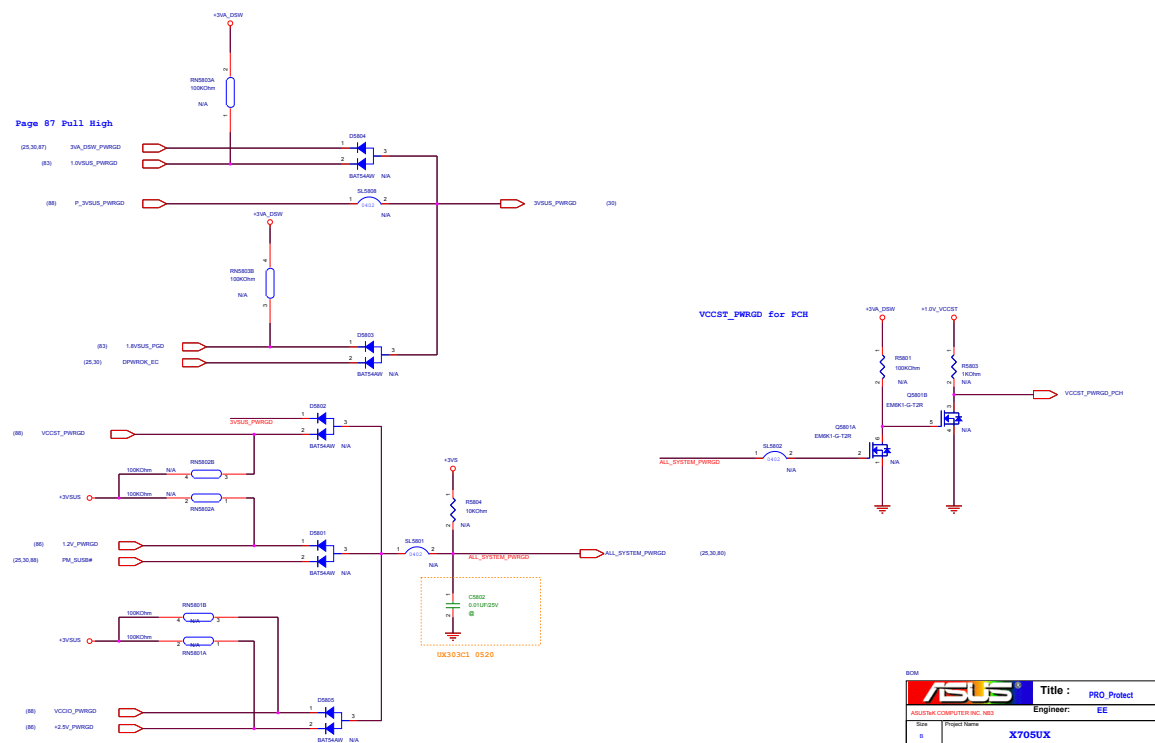
076

Toggle FullScreen

Pre Page

Next Page

MoveTo



Schematic Page List

Hide

U68_

069_*****

070_GPU_PCI-EXPRESS

071_GPU_MEMORY INTERFACE

072_VGA_GDDR5_(Bit0~63)

073_VGA_GDDR5_(NA)

074_VGA_nVIDIA_N16V/S_DISPL
AY

075_GPU_GPIO_THERM

076_GPU_XTAL/STRAPPING

077_GPU_PWG/GND

078_GPU_Power_Sel

079_*****

080_PW_KABY LAKE-U(1)

081_PW_KABY LAKE-U(2)

082_PW_***

083_PW_+1.0VSUS/1.8VSUS(UP
9002)

084_PW_

085_PW_***

086_PW_1.2V/+VTT/+2.5V(UP900
2)

Global Search

057

058

060

061

065

070

071

072

074

075

076

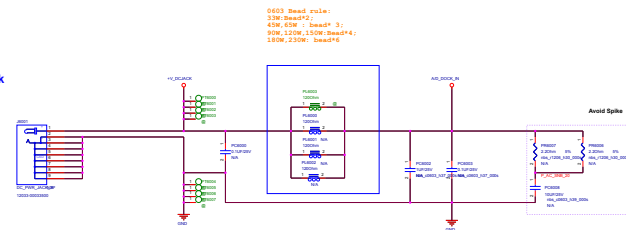
Toggle FullScreen

Pre Page

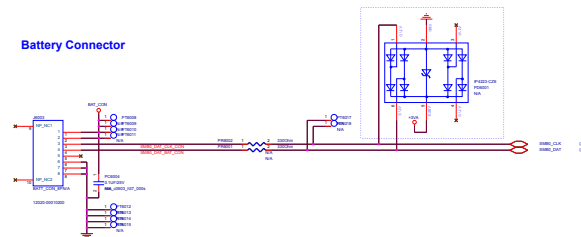
Next Page

MoveTo

DC Jack

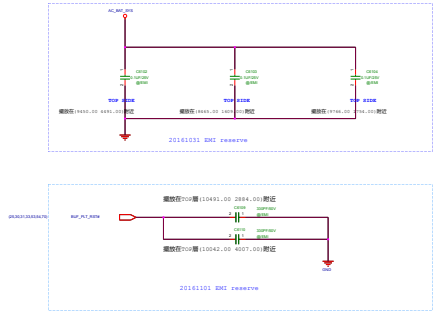


Battery Connector



Schematic Page List	Hide
U68_	
069_*****	
070_GPU_PCI-EXPRESS	
071_GPU_MEMORY INTERFACE	
072_VGA_GDDR5_(Bit0~63)	
073_VGA_GDDR5_(NA)	
074_VGA_nVIDIA_N16V/S_DISPLAY	
075_GPU_GPIO_THERM	
076_GPU_XTAL/STRAPPING	
077_GPU_PWG/GND	
078_GPU_Power_Sel	
079_*****	
080_PW_KABY LAKE-U(1)	
081_PW_KABY LAKE-U(2)	
082_PW_***	
083_PW_+1.0VSUS/1.8VSUS(UP9002)	
084_PW_	
085_PW_***	
086_PW_1.2V/+VTT/+2.5V(UP9002)	

Global Search	057	058	060	061	065	070	071	072	074	075	076
Toggle FullScreen						Pre Page		Next Page		MoveTo	



Schematic Page List

Hide

U68_
069_*****

070_GPU_PCI-EXPRESS
071_GPU_MEMORY INTERFACE
072_VGA_GDDR5_(Bit0~63)
073_VGA_GDDR5_(NA)
074_VGA_nVIDIA_N16V/S_DISPL
AY
075_GPU_GPIO_THERM
076_GPU_XTAL/STRAPPING
077_GPU_PWG/GND
078_GPU_Power_Sel
079_*****
080_PW_KABY LAKE-U(1)
081_PW_KABY LAKE-U(2)
082_PW_***
083_PW_+1.0VSUS/1.8VSUS(UP
9002)
084_PW_
085_PW_***
086_PW_1.2V/+VTT/+2.5V(UP900
2)

Global Search

057

058

060

061

065

070

071

072

074

075

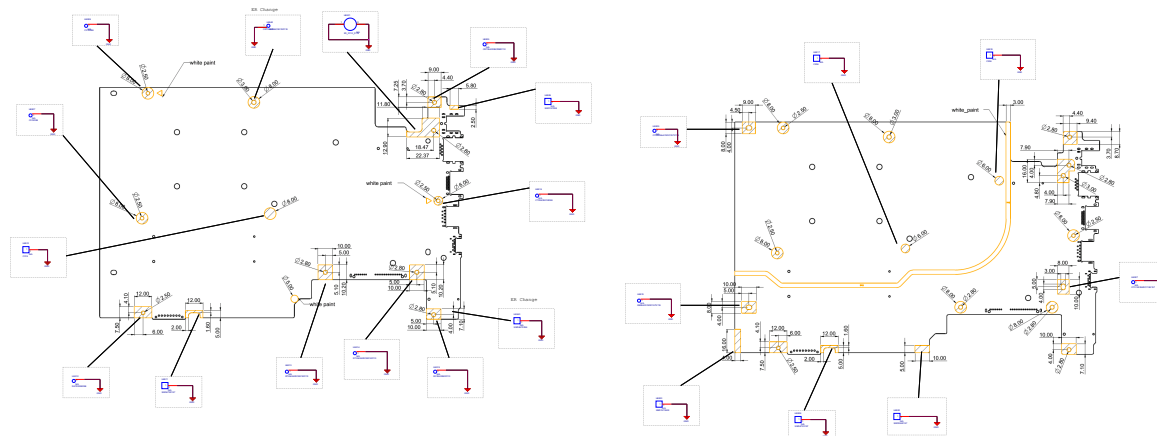
076

Toggle FullScreen

Pre Page

Next Page

MoveTo



Schematic Page List

Hide

U68_

069_*****

070_GPU_PCI-EXPRESS

071_GPU_MEMORY INTERFACE

072_VGA_GDDR5_(Bit0~63)

073_VGA_GDDR5_(NA)

074_VGA_nVIDIA_N16V/S_DISPLAY

075_GPU_GPIO_THERM

076_GPU_XTAL/STRAPPING

077_GPU_PWG/GND

078_GPU_Power_Sel

079_*****

080_PW_KABY LAKE-U(1)

081_PW_KABY LAKE-U(2)

082_PW_***

083_PW_+1.0VSUS/1.8VSUS(UP9002)

084_PW_

085_PW_***

086_PW_1.2V/+VTT/+2.5V(UP9002)

Global Search

057

058

060

061

065

070

071

072

074

075

076

Toggle FullScreen

Pre Page

Next Page

MoveTo

PCI EXPRESS

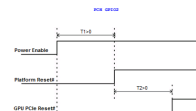
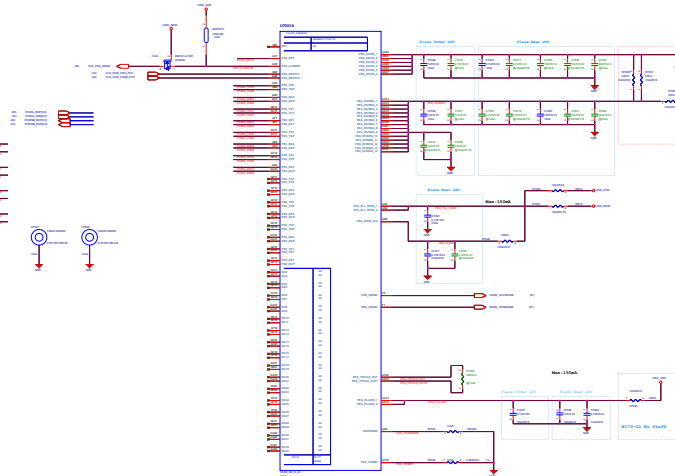
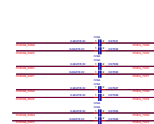


Figure 18-4. Cold Reset Sequence Requirement for Optimus

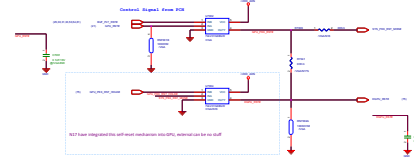


Table 6. PEX Core and IO Supply Decoupling and Filtering

GPU	Capacitor Type	Footprint	Population		Location
			N16	N17	
N16_PEX_IOWDD (N17_PEX_IOWDD) Supply Rail					
GB2B-64, GB2C-64	1.0 μ F	X65	0402	1	Under GPU
	4.7 μ F	X65	0603	0	Under GPU
	4.7 μ F	X65	0603	1	Near GPU
	10 μ F	X65	0805	0	Midway between GPU and Power Supply
	22 μ F	X65	0805	0	Midway between GPU and Power Supply
N16_PEX_IOWDDQ (N17_PEX_IOWDDQ) Supply Rail					
GB2B-64, GB2C-64	1.0 μ F	X65	0402	1	Under GPU
	4.7 μ F	X65	0603	1	Near GPU
	10 μ F	X65	0805LP	1	Midway between GPU and Power Supply
	22 μ F	X65	0805LP	1	Midway between GPU and Power Supply

Note: All GPIO, DCA/B/C/S, DAC, H/V, SYNC, PCIe Reset, CLKREQ#, XTAL, and JTAG signals, when pulled-up, should be connected to VTX_A0H power rail. Also PEX_PLL_IOWDD and PEX_VTX_IOWDD must be sourced by VTX_A0H.

teknisi-indonesia

Table 7. PEX PLLs Decoupling and Filtering

GPU	Capacitor Type	Footprint	Population		Location
			N36	N17	
PEX_PLLVDD Supply Rail					
GB2B-64	0.1 μ F	X7R	0402	1	N/A Under GPU
	1.0 μ F	X5R	0603	1	N/A Body GPU
	4.7 μ F	X5R	0805	1	N/A Near GPU
PEX_IOWDD_3V3 Supply Rail					
GB2B-64	4.7 μ F	X5R	0603	2	N/A Near GPU
PEX_PLL_IOWDD Supply Rail					
GB2B-64, GB2C-64	0.1 μ F	X7R	0402	1	Near GPU



Schematic Page List

Hide

U68_

069_*****

070_GPU_PCI-EXPRESS

071_GPU_MEMORY INTERFACE

072_VGA_GDDR5_(Bit0~63)

073_VGA_GDDR5_(NA)

074_VGA_nVIDIA_N16V/S_DISPL
AY

075_GPU_GPIO_THERM

076_GPU_XTAL/STRAPPING

077_GPU_PWG/GND

078_GPU_Power_Sel

079_*****

080_PW_KABY LAKE-U(1)

081_PW_KABY LAKE-U(2)

082_PW_***

083_PW_+1.0VSUS/1.8VSUS(UP
9002)

084_PW_

085_PW_***

086_PW_1.2V/+VTT/+2.5V(UP900
2)

Global Search

057

058

060

061

065

070

071

072

074

075

076

Toggle FullScreen

Pre Page

Next Page

MoveTo

GPU MEMORY INTERFACE: PARTITION A

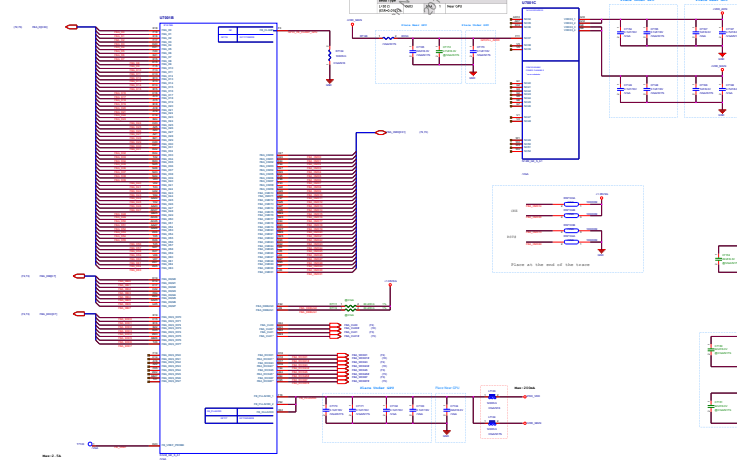


Table 5. Frame Buffer PLLs Decoupling and Filtering

GPU	Capacitor Type	Footprint	Population N16	N17	Location
FB PLL Supply	0.1 uF X7R	0402	2	4	Under GPU
GB2B-64, GB2C-64	22 uF X6S	0603	1	1	Near GPU
Bead Type	30 13 (ESR<0.001nH)	0603	1	1	Near GPU

Table 4. Frame Buffer Core and IO Decoupling and Filtering

GPU	Capacitor Type	Footprint	Population N16	N17	Location
FBVDD/IQ Supply Rail for GDDR5	0.1 uF X7R	0402	2	0	Near GPU
GB2B-64, GB2C-64	1 uF X7R	0603	2	0	Under GPU
	4.7 uF X6S	0603	2	0	Under GPU
	10 uF X6S	0603	0	2	Under GPU
	10 uF X6S	0603	1	1	Near GPU
	22 uF X6S	0603W	1	3	Near GPU

GPU	Capacitor Type	Footprint	Population N16	N17	Location
N16 3V3_AIN (N17 VDD18) Supply Rail	0.1 uF X7R	0402	2	2	Under GPU
GB2B-64, GB2C-64	1.0 uF X6S	0603	1	1	Near GPU
	4.7 uF X6S	0603	1	1	Near GPU
N16 3V3_AIN (N17 1V8_AIN) Supply Rail	0.1 uF X7R	0402	0	1	Under GPU
GB2B-64, GB2C-64	1.0 uF X6S	0603	1	1	Near GPU
	4.7 uF X6S	0603	1	1	Near GPU

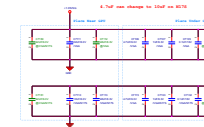


Table 7. GB2B-64/GB2C-64 Capacitors for Frame Buffer Interface

Capacitor Type	Footprint	Population N16	N17	Location
----------------	-----------	----------------	-----	----------



Schematic Page List

Hide

U68_

069_*****

070_GPU_PCI-EXPRESS

071_GPU_MEMORY INTERFACE

072_VGA_GDDR5_(Bit0~63)

073_VGA_GDDR5_(NA)

074_VGA_nVIDIA_N16V/S_DISPL
AY

075_GPU_GPIO_THERM

076_GPU_XTAL/STRAPPING

077_GPU_PWG/GND

078_GPU_Power_Sel

079_*****

080_PW_KABY LAKE-U(1)

081_PW_KABY LAKE-U(2)

082_PW_***

083_PW_+1.0VSUS/1.8VSUS(UP
9002)

084_PW_

085_PW_***

086_PW_1.2V/+VTT/+2.5V(UP900
2)

Global Search

057

058

060

061

065

070

071

072

074

075

076

Toggle FullScreen

Pre Page

Next Page

MoveTo

GDDR5 X32 (Bit0:63)

Pin 20 VDDH (GDDR5) G2

Pin 21 GND

Pin 22 GND

Pin 23 GND

Pin 24 GND

Pin 25 GND

Pin 26 GND

Pin 27 GND

Pin 28 GND

Pin 29 GND

Pin 30 GND

Pin 31 GND

Pin 32 GND

Pin 33 GND

Pin 34 GND

Pin 35 GND

Pin 36 GND

Pin 37 GND

Pin 38 GND

Pin 39 GND

Pin 40 GND

Pin 41 GND

Pin 42 GND

Pin 43 GND

Pin 44 GND

Pin 45 GND

Pin 46 GND

Pin 47 GND

Pin 48 GND

Pin 49 GND

Pin 50 GND

Pin 51 GND

Pin 52 GND

Pin 53 GND

Pin 54 GND

Pin 55 GND

Pin 56 GND

Pin 57 GND

Pin 58 GND

Pin 59 GND

Pin 60 GND

Pin 61 GND

Pin 62 GND

Pin 63 GND

Pin 64 GND

Pin 65 GND

Pin 66 GND

Pin 67 GND

Pin 68 GND

Pin 69 GND

Pin 70 GND

Pin 71 GND

Pin 72 GND

Pin 73 GND

Pin 74 GND

Pin 75 GND

Pin 76 GND

Pin 77 GND

Pin 78 GND

Pin 79 GND

Pin 80 GND

Pin 81 GND

Pin 82 GND

Pin 83 GND

Pin 84 GND

Pin 85 GND

Pin 86 GND

Pin 87 GND

Pin 88 GND

Pin 89 GND

Pin 90 GND

Pin 91 GND

Pin 92 GND

Pin 93 GND

Pin 94 GND

Pin 95 GND

Pin 96 GND

Pin 97 GND

Pin 98 GND

Pin 99 GND

Pin 100 GND

Pin 101 GND

Pin 102 GND

Pin 103 GND

Pin 104 GND

Pin 105 GND

Pin 106 GND

Pin 107 GND

Pin 108 GND

Pin 109 GND

Pin 110 GND

Pin 111 GND

Pin 112 GND

Pin 113 GND

Pin 114 GND

Pin 115 GND

Pin 116 GND

Pin 117 GND

Pin 118 GND

Pin 119 GND

Pin 120 GND

Pin 121 GND

Pin 122 GND

Pin 123 GND

Pin 124 GND

Pin 125 GND

Pin 126 GND

Pin 127 GND

Pin 128 GND

Pin 129 GND

Pin 130 GND

Pin 131 GND

Pin 132 GND

Pin 133 GND

Pin 134 GND

Pin 135 GND

Pin 136 GND

Pin 137 GND

Pin 138 GND

Pin 139 GND

Pin 140 GND

Pin 141 GND

Pin 142 GND

Pin 143 GND

Pin 144 GND

Pin 145 GND

Pin 146 GND

Pin 147 GND

Pin 148 GND

Pin 149 GND

Pin 150 GND

Pin 151 GND

Pin 152 GND

Pin 153 GND

Pin 154 GND

Pin 155 GND

Pin 156 GND

Pin 157 GND

Pin 158 GND

Pin 159 GND

Pin 160 GND

Pin 161 GND

Pin 162 GND

Pin 163 GND

Pin 164 GND

Pin 165 GND

Pin 166 GND

Pin 167 GND

Pin 168 GND

Pin 169 GND

Pin 170 GND

Pin 171 GND

Pin 172 GND

Pin 173 GND

Pin 174 GND

Pin 175 GND

Pin 176 GND

Pin 177 GND

Pin 178 GND

Pin 179 GND

Pin 180 GND

Pin 181 GND

Pin 182 GND

Pin 183 GND

Pin 184 GND

Pin 185 GND

Pin 186 GND

Pin 187 GND

Pin 188 GND

Pin 189 GND

Pin 190 GND

Pin 191 GND

Pin 192 GND

Pin 193 GND

Pin 194 GND

Pin 195 GND

Pin 196 GND

Pin 197 GND

Pin 198 GND

Pin 199 GND

Pin 200 GND

Pin 201 GND

Pin 202 GND

Pin 203 GND

Pin 204 GND

Pin 205 GND

Pin 206 GND

Pin 207 GND

Pin 208 GND

Pin 209 GND

Pin 210 GND

Pin 211 GND

Pin 212 GND

Pin 213 GND

Pin 214 GND

Pin 215 GND

Pin 216 GND

Pin 217 GND

Pin 218 GND

Pin 219 GND

Pin 220 GND

Pin 221 GND

Pin 222 GND

Pin 223 GND

Pin 224 GND

Pin 225 GND

Pin 226 GND

Pin 227 GND

Pin 228 GND

Pin 229 GND

Pin 230 GND

Pin 231 GND

Pin 232 GND

Pin 233 GND

Pin 234 GND

Pin 235 GND

Pin 236 GND

Pin 237 GND

Pin 238 GND

Pin 239 GND

Pin 240 GND

Pin 241 GND

Pin 242 GND

Pin 243 GND

Pin 244 GND

Pin 245 GND

Pin 246 GND

Pin 247 GND

Pin 248 GND

Pin 249 GND

Pin 250 GND

Pin 251 GND

Pin 252 GND

Pin 253 GND

Pin 254 GND

Pin 255 GND

Pin 256 GND

Pin 257 GND

Pin 258 GND

Pin 259 GND

Pin 260 GND

Pin 261 GND

Pin 262 GND

Pin 263 GND

Pin 264 GND

Pin 265 GND

Pin 266 GND

Pin 267 GND

Pin 268 GND

Pin 269 GND

Pin 270 GND

Pin 271 GND

Pin 272 GND

Pin 273 GND

Pin 274 GND

Pin 275 GND

Pin 276 GND

Pin 277 GND

Pin 278 GND

Pin 279 GND

Pin 280 GND

Pin 281 GND

Pin 282 GND

Pin 283 GND

Pin 284 GND

Pin 285 GND

Pin 286 GND

Pin 287 GND

Pin 288 GND

Pin 289 GND

Pin 290 GND

Pin 291 GND

Pin 292 GND

Pin 293 GND

Pin 294 GND

Pin 295 GND

Pin 296 GND

Pin 297 GND

Pin 298 GND

Pin 299 GND

Pin 300 GND

Pin 301 GND

Pin 302 GND

Pin 303 GND

Pin 304 GND

Pin 305 GND

Pin 306 GND

Pin 307 GND

Pin 308 GND

Pin 309 GND

Pin 310 GND

Pin 311 GND

Pin 312 GND

Pin 313 GND

Pin 314 GND

Pin 315 GND</

Schematic Page List

Hide

U68_

069_*****

070_GPU_PCI-EXPRESS

071_GPU_MEMORY INTERFACE

072_VGA_GDDR5_(Bit0~63)

073_VGA_GDDR5_(NA)

074_VGA_nVIDIA_N16V/S_DISPL
AY

075_GPU_GPIO_THERM

076_GPU_XTAL/STRAPPING

077_GPU_PWG/GND

078_GPU_Power_Sel

079_*****

080_PW_KABY LAKE-U(1)

081_PW_KABY LAKE-U(2)

082_PW_***

083_PW_+1.0VSUS/1.8VSUS(UP
9002)

084_PW_

085_PW_***

086_PW_1.2V/+VTT/+2.5V(UP900
2)

Global Search

057

058

060

061

065

070

071

072

074

075

076

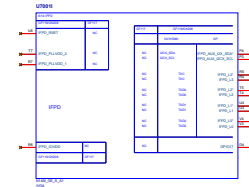
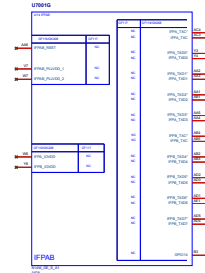
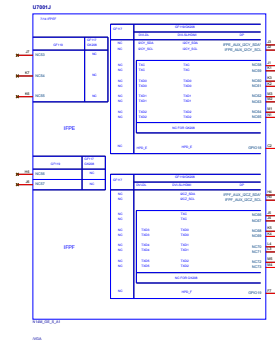
Toggle FullScreen

Pre Page

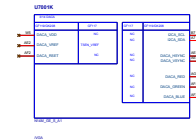
Next Page

MoveTo

LVDS

www.teknisi-indonesia.com

CRT



Schematic Page List

Hide

068_

069 *****

070 GPU PCI-EXPRESS

071 GPU MEMORY INTERFACE

072_VGA_GDDR5_(Bit0~63)

073 VGA GDDR5 (NA)

074_VGA_nVIDIA_N16V/S_DISPL
AY

075 GPU GPIO THERM

076 GPU XTAL/STRAPPING

077 GPU PWG/GND

078 GPU Power Sel

079 *****

080 PW KABY LAKE-U(1)

081_PW_KABY LAKE-U(2)

082 PW ***

083_PW_+1.0VSUS/1.8VSUS(UP
9002)

084_PW_

085_PW_***

086_PW_1.2V/+VTT/+2.5V(UP900
2)

Global Search

057

058

060

061

065

070

071

072

074

075

076

Toggle FullScreen

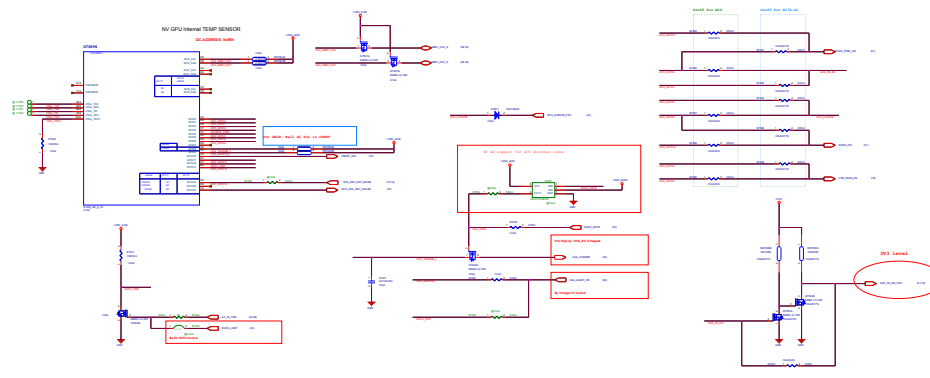
Pre Page

Next Page

MoveTo

Table 12-2. G2024-64 and OpenStack 2024 Description			
OpenStack	OSD Name	OSD Description	OSD Termination
OSD0	OSD_0_0	OS 0: Cluster for 12 OSDs	14:00 per day
OSD1	OSD_1_0	OS 1: Cluster for 12 OSDs	14:00 per day
OSD2	OSD_2_0	OS 2: Cluster for 12 OSDs	14:00 per day
OSD3	OSD_3_0	OS 3: Cluster for 12 OSDs	14:00 per day
OSD4	OSD_4_0	OS 4: Cluster for 12 OSDs	14:00 per day
OSD5	OSD_5_0	OS 5: Cluster for 12 OSDs	14:00 per day
OSD6	OSD_6_0	OS 6: Cluster for 12 OSDs	14:00 per day
OSD7	OSD_7_0	OS 7: Cluster for 12 OSDs	14:00 per day
OSD8	OSD_8_0	OS 8: Cluster for 12 OSDs	14:00 per day
OSD9	OSD_9_0	OS 9: Cluster for 12 OSDs	14:00 per day
OSD10	OSD_10_0	OS 10: Cluster for 12 OSDs	14:00 per day
OSD11	OSD_11_0	OS 11: Cluster for 12 OSDs	14:00 per day
OSD12	OSD_12_0	OS 12: Cluster for 12 OSDs	14:00 per day
OSD13	OSD_13_0	OS 13: Cluster for 12 OSDs	14:00 per day
OSD14	OSD_14_0	OS 14: Cluster for 12 OSDs	14:00 per day
OSD15	OSD_15_0	OS 15: Cluster for 12 OSDs	14:00 per day
OSD16	OSD_16_0	OS 16: Cluster for 12 OSDs	14:00 per day
OSD17	OSD_17_0	OS 17: Cluster for 12 OSDs	14:00 per day
OSD18	OSD_18_0	OS 18: Cluster for 12 OSDs	14:00 per day
OSD19	OSD_19_0	OS 19: Cluster for 12 OSDs	14:00 per day
OSD20	OSD_20_0	OS 20: Cluster for 12 OSDs	14:00 per day
OSD21	OSD_21_0	OS 21: Cluster for 12 OSDs	14:00 per day
OSD22	OSD_22_0	OS 22: Cluster for 12 OSDs	14:00 per day
OSD23	OSD_23_0	OS 23: Cluster for 12 OSDs	14:00 per day
OSD24	OSD_24_0	OS 24: Cluster for 12 OSDs	14:00 per day
OSD25	OSD_25_0	OS 25: Cluster for 12 OSDs	14:00 per day
OSD26	OSD_26_0	OS 26: Cluster for 12 OSDs	14:00 per day
OSD27	OSD_27_0	OS 27: Cluster for 12 OSDs	14:00 per day
OSD28	OSD_28_0	OS 28: Cluster for 12 OSDs	14:00 per day
OSD29	OSD_29_0	OS 29: Cluster for 12 OSDs	14:00 per day
OSD30	OSD_30_0	OS 30: Cluster for 12 OSDs	14:00 per day
OSD31	OSD_31_0	OS 31: Cluster for 12 OSDs	14:00 per day
OSD32	OSD_32_0	OS 32: Cluster for 12 OSDs	14:00 per day
OSD33	OSD_33_0	OS 33: Cluster for 12 OSDs	14:00 per day
OSD34	OSD_34_0	OS 34: Cluster for 12 OSDs	14:00 per day
OSD35	OSD_35_0	OS 35: Cluster for 12 OSDs	14:00 per day
OSD36	OSD_36_0	OS 36: Cluster for 12 OSDs	14:00 per day
OSD37	OSD_37_0	OS 37: Cluster for 12 OSDs	14:00 per day
OSD38	OSD_38_0	OS 38: Cluster for 12 OSDs	14:00 per day
OSD39	OSD_39_0	OS 39: Cluster for 12 OSDs	14:00 per day
OSD40	OSD_40_0	OS 40: Cluster for 12 OSDs	14:00 per day
OSD41	OSD_41_0	OS 41: Cluster for 12 OSDs	14:00 per day
OSD42	OSD_42_0	OS 42: Cluster for 12 OSDs	14:00 per day
OSD43	OSD_43_0	OS 43: Cluster for 12 OSDs	14:00 per day
OSD44	OSD_44_0	OS 44: Cluster for 12 OSDs	14:00 per day
OSD45	OSD_45_0	OS 45: Cluster for 12 OSDs	14:00 per day
OSD46	OSD_46_0	OS 46: Cluster for 12 OSDs	14:00 per day
OSD47	OSD_47_0	OS 47: Cluster for 12 OSDs	14:00 per day
OSD48	OSD_48_0	OS 48: Cluster for 12 OSDs	14:00 per day
OSD49	OSD_49_0	OS 49: Cluster for 12 OSDs	14:00 per day
OSD50	OSD_50_0	OS 50: Cluster for 12 OSDs	14:00 per day
OSD51	OSD_51_0	OS 51: Cluster for 12 OSDs	14:00 per day
OSD52	OSD_52_0	OS 52: Cluster for 12 OSDs	14:00 per day
OSD53	OSD_53_0	OS 53: Cluster for 12 OSDs	14:00 per day
OSD54	OSD_54_0	OS 54: Cluster for 12 OSDs	14:00 per day
OSD55	OSD_55_0	OS 55: Cluster for 12 OSDs	14:00 per day
OSD56	OSD_56_0	OS 56: Cluster for 12 OSDs	14:00 per day
OSD57	OSD_57_0	OS 57: Cluster for 12 OSDs	14:00 per day
OSD58	OSD_58_0	OS 58: Cluster for 12 OSDs	14:00 per day
OSD59	OSD_59_0	OS 59: Cluster for 12 OSDs	14:00 per day
OSD60	OSD_60_0	OS 60: Cluster for 12 OSDs	14:00 per day
OSD61	OSD_61_0	OS 61: Cluster for 12 OSDs	14:00 per day
OSD62	OSD_62_0	OS 62: Cluster for 12 OSDs	14:00 per day
OSD63	OSD_63_0	OS 63: Cluster for 12 OSDs	14:00 per day
OSD64	OSD_64_0	OS 64: Cluster for 12 OSDs	14:00 per day
OSD65	OSD_65_0	OS 65: Cluster for 12 OSDs	14:00 per day
OSD66	OSD_66_0	OS 66: Cluster for 12 OSDs	14:00 per day
OSD67	OSD_67_0	OS 67: Cluster for 12 OSDs	14:00 per day
OSD68	OSD_68_0	OS 68: Cluster for 12 OSDs	14:00 per day
OSD69	OSD_69_0	OS 69: Cluster for 12 OSDs	14:00 per day
OSD70	OSD_70_0	OS 70: Cluster for 12 OSDs	14:00 per day
OSD71	OSD_71_0	OS 71: Cluster for 12 OSDs	14:00 per day
OSD72	OSD_72_0	OS 72: Cluster for 12 OSDs	14:00 per day
OSD73	OSD_73_0	OS 73: Cluster for 12 OSDs	14:00 per day
OSD74	OSD_74_0	OS 74: Cluster for 12 OSDs	14:00 per day
OSD75	OSD_75_0	OS 75: Cluster for 12 OSDs	14:00 per day
OSD76	OSD_76_0	OS 76: Cluster for 12 OSDs	14:00 per day
OSD77	OSD_77_0	OS 77: Cluster for 12 OSDs	14:00 per day
OSD78	OSD_78_0	OS 78: Cluster for 12 OSDs	14:00 per day
OSD79	OSD_79_0	OS 79: Cluster for 12 OSDs	14:00 per day
OSD80	OSD_80_0	OS 80: Cluster for 12 OSDs	14:00 per day
OSD81	OSD_81_0	OS 81: Cluster for 12 OSDs	14:00 per day
OSD82	OSD_82_0	OS 82: Cluster for 12 OSDs	14:00 per day
OSD83	OSD_83_0	OS 83: Cluster for 12 OSDs	14:00 per day
OSD84	OSD_84_0	OS 84: Cluster for 12 OSDs	14:00 per day
OSD85	OSD_85_0	OS 85: Cluster for 12 OSDs	14:00 per day
OSD86	OSD_86_0	OS 86: Cluster for 12 OSDs	14:00 per day
OSD87	OSD_87_0	OS 87: Cluster for 12 OSDs	14:00 per day
OSD88	OSD_88_0	OS 88: Cluster for 12 OSDs	14:00 per day
OSD89	OSD_89_0	OS 89: Cluster for 12 OSDs	14:00 per day
OSD90	OSD_90_0	OS 90: Cluster for 12 OSDs	14:00 per day
OSD91	OSD_91_0	OS 91: Cluster for 12 OSDs	14:00 per day
OSD92	OSD_92_0	OS 92: Cluster for 12 OSDs	14:00 per day
OSD93	OSD_93_0	OS 93: Cluster for 12 OSDs	14:00 per day
OSD94	OSD_94_0	OS 94: Cluster for 12 OSDs	14:00 per day
OSD95	OSD_95_0	OS 95: Cluster for 12 OSDs	14:00 per day
OSD96	OSD_96_0	OS 96: Cluster for 12 OSDs	14:00 per day
OSD97	OSD_97_0	OS 97: Cluster for 12 OSDs	14:00 per day
OSD98	OSD_98_0	OS 98: Cluster for 12 OSDs	14:00 per day
OSD99	OSD_99_0	OS 99: Cluster for 12 OSDs	14:00 per day
OSD100	OSD_100_0	OS 100: Cluster for 12 OSDs	14:00 per day
OSD101	OSD_101_0	OS 101: Cluster for 12 OSDs	14:00 per day
OSD102	OSD_102_0	OS 102: Cluster for 12 OSDs	14:00 per day
OSD103	OSD_103_0	OS 103: Cluster for 12 OSDs	14:00 per day
OSD104	OSD_104_0	OS 104: Cluster for 12 OSDs	14:00 per day
OSD105	OSD_105_0	OS 105: Cluster for 12 OSDs	14:00 per day
OSD106	OSD_106_0	OS 106: Cluster for 12 OSDs	14:00 per day
OSD107	OSD_107_0	OS 107: Cluster for 12 OSDs	14:00 per day
OSD108	OSD_108_0	OS 108: Cluster for 12 OSDs	14:00 per day
OSD109	OSD_109_0	OS 109: Cluster for 12 OSDs	14:00 per day
OSD110	OSD_110_0	OS 110: Cluster for 12 OSDs	14:00 per day
OSD111	OSD_111_0	OS 111: Cluster for 12 OSDs	14:00 per day
OSD112	OSD_112_0	OS 112: Cluster for 12 OSDs	14:00 per day
OSD113	OSD_113_0	OS 113: Cluster for 12 OSDs	14:00 per day
OSD114	OSD_114_0	OS 114: Cluster for 12 OSDs	14:00 per day
OSD115	OSD_115_0	OS 115: Cluster for 12 OSDs	14:00 per day
OSD116	OSD_116_0	OS 116: Cluster for 12 OSDs	14:00 per day
OSD117	OSD_117_0	OS 117: Cluster for 12 OSDs	14:00 per day
OSD118	OSD_118_0	OS 118: Cluster for 12 OSDs	14:00 per day
OSD119	OSD_119_0	OS 119: Cluster for 12 OSDs	14:00 per day
OSD120	OSD_120_0	OS 120: Cluster for 12 OSDs	14:00 per day
OSD121	OSD_121_0	OS 121: Cluster for 12 OSDs	14:00 per day
OSD122	OSD_122_0	OS 122: Cluster for 12 OSDs	14:00 per day
OSD123	OSD_123_0	OS 123: Cluster for 12 OSDs	14:00 per day
OSD124	OSD_124_0	OS 124: Cluster for 12 OSDs	14:00 per day
OSD125	OSD_125_0	OS 125: Cluster for 12 OSDs	14:00 per day
OSD126	OSD_126_0	OS 126: Cluster for 12 OSDs	14:00 per day
OSD127	OSD_127_0	OS 127: Cluster for 12 OSDs	14:00 per day
OSD128	OSD_128_0	OS 128: Cluster for 12 OSDs	14:00 per day
OSD129	OSD_129_0	OS 129: Cluster for 12 OSDs	14:00 per day
OSD130	OSD_130_0	OS 130: Cluster for 12 OSDs	14:00 per day
OSD131	OSD_131_0	OS 131: Cluster for 12 OSDs	14:00 per day
OSD132	OSD_132_0	OS 132: Cluster for 12 OSDs	14:00 per day
OSD133	OSD_133_0	OS 133: Cluster for 12 OSDs	14:00 per day
OSD134	OSD_134_0	OS 134: Cluster for 12 OSDs	14:00 per day
OSD135	OSD_135_0	OS 135: Cluster for 12 OSDs	14:00 per day
OSD136	OSD_136_0	OS 136: Cluster for 12 OSDs	14:00 per day
OSD137	OSD_137_0	OS 137: Cluster for 12 OSDs	14:00 per day
OSD138	OSD_138_0	OS 138: Cluster for 12 OSDs	14:00 per day
OSD139	OSD_139_0	OS 139: Cluster for 12 OSDs	14:00 per day
OSD140	OSD_140_0	OS 140: Cluster for 12 OSDs	14:00 per day
OSD141	OSD_141_0	OS 141: Cluster for 12 OSDs	14:00 per day
OSD142	OSD_142_0	OS 142: Cluster for 12 OSDs	14:00 per day
OSD143	OSD_143_0	OS 143: Cluster for 12 OSDs	14:00 per day
OSD144	OSD_144_0	OS 144: Cluster for 12 OSDs	14:00 per day
OSD145	OSD_145_0	OS 145: Cluster for 12 OSDs	14:00 per day
OSD146	OSD_146_0	OS 146: Cluster for 12 OSDs	14:00 per day
OSD147	OSD_147_0	OS 147: Cluster for 12 OSDs	14:00 per day
OSD148	OSD_148_0	OS 148: Cluster for 12 OSDs	14:00 per day
OSD149	OSD_149_0	OS 149: Cluster for 12 OSDs	14:00 per day
OSD150	OSD_150_0	OS 150: Cluster for 12 OSDs	14:00 per day
OSD151	OSD_151_0	OS 151: Cluster for 12 OSDs	14:00 per day
OSD152	OSD_152_0	OS 152: Cluster for 12 OSDs	14:00 per day
OSD153	OSD_153_0	OS 153: Cluster for 12 OSDs	14:00 per day
OSD154	OSD_154_0	OS 154: Cluster for 12 OSDs	14:00 per day
OSD155	OSD_155_0	OS 155: Cluster for 12 OSDs	14:00 per day
OSD156	OSD_156_0	OS 156: Cluster for 12 OSDs	14:00 per day
OSD157	OSD_157_0	OS 157: Cluster for 12 OSDs	14:00 per day
OSD158	OSD_158_0	OS 158: Cluster for 12 OSDs	14:00 per day
OSD159	OSD_159_0	OS 159: Cluster for 12 OSDs	14:00 per day
OSD160	OSD_160_0	OS 160: Cluster for 12 OSDs	14:00 per day
OSD161	OSD_161_0	OS 161: Cluster for 12 OSDs	14:00 per day
OSD162	OSD_162_0	OS 162: Cluster for 12 OSDs	14:00 per day
OSD163	OSD_163_0	OS 163: Cluster for 12 OSDs	14:00 per day
OSD164	OSD_164_0	OS 164: Cluster for 12 OSDs	14:00 per day
OSD165	OSD_165_0	OS 165: Cluster for 12 OSDs	14:00 per day
OSD166	OSD_166_0	OS 166: Cluster for 12 OSDs	14:00 per day
OSD167	OSD_167_0	OS 167: Cluster for 12 OSDs	14:00 per day
OSD168	OSD_168_0	OS 168: Cluster for 12 OSDs	14:00 per day
OSD169	OSD_169_0	OS 169: Cluster for 12 OSDs	14:00 per day
OSD170	OSD_170_0	OS 170: Cluster for 12 OSDs	14:00 per day
OSD171	OSD_171_0	OS 171: Cluster for 12 OSDs	14:00 per day
OSD172	OSD_172_0	OS 172: Cluster for 12 OSDs	14:00 per day
OSD173	OSD_173_0	OS 173: Cluster for 12 OSDs	14:00 per day
OSD174	OSD_174_0	OS 174: Cluster for 12 OSDs	14:00 per day
OSD175	OSD_175_0	OS 175: Cluster for 12 OSDs	14:00 per day
OSD176	OSD_176_0	OS 176: Cluster for 12 OSDs	14:00 per day
OSD177	OSD_177_0	OS 177: Cluster for 12 OSDs	14:00 per day
OSD178	OSD_178_0	OS 178: Cluster for 12 OSDs	14:00 per day
OSD179	OSD_179_0	OS 179: Cluster for 12 OSDs	14:00 per day
OSD180	OSD_180_0	OS 180: Cluster for 12 OSDs	14:00 per day
OSD181	OSD_181_0	OS 181: Cluster for 12 OSDs	14:00 per day
OSD182	OSD_182_0	OS 182: Cluster for 12 OSDs	14:00 per day
OSD183	OSD_183_0	OS 183: Cluster for 12 OSDs	14:00 per day
OSD184	OSD_184_0	OS 184: Cluster for 12 OSDs	14:00 per day
OSD185	OSD_185_0	OS 185: Cluster for 12 OSDs	14:00 per day
OSD186	OSD_186_0	OS 186: Cluster for 12 OSDs	14:00 per day
OSD187	OSD_187_0	OS 187: Cluster for 12 OSDs	14:00 per day
OSD188	OSD_188_0	OS 188: Cluster for 12 OSDs	14:00 per day
OSD189	OSD_189_0	OS 189: Cluster for 12 OSDs	14:00 per day
OSD190	OSD_190_0	OS 190: Cluster for 12 OSDs	14:00 per day
OSD191	OSD_191_0	OS 191: Cluster for 12 OSDs	14:00 per day
OSD192	OSD_192_0	OS 192: Cluster for 12 OSDs	14:00 per day
OSD193	OSD_193_0	OS 193: Cluster for 12 OSDs	14:00 per day
OSD194	OSD_194_0	OS 194: Cluster for 12 OSDs	14:00 per day
OSD195	OSD_195_0	OS 195: Cluster for 12 OSDs	14:00 per day
OSD196	OSD_196_0	OS 196: Cluster for 12 OSDs	14:00 per day
OSD197	OSD_197_0	OS 197: Cluster for 12 OSDs	14:00 per day
OSD198	OSD_198_0	OS 198: Cluster for 12 OSDs	14:00 per day
OSD199	OSD_199_0	OS 199: Cluster for 12 OSDs	14:00 per day
OSD200	OSD_200_0	OS 200: Cluster for 12 OSDs	14:00 per day
OSD201	OSD_201_0	OS 201: Cluster for 12 OSDs	14:00 per day
OSD202	OSD_202_0	OS 202: Cluster for 12 OSDs	14:00 per day
OSD203	OSD_203_0	OS 203: Cluster for 12 OSDs	14:00 per day
OSD204	OSD_204_0	OS 204: Cluster for 12 OSDs	14:00 per day
OSD205	OSD_205_0	OS 205: Cluster for 12 OSDs	14:00 per day
OSD206	OSD_206_0	OS 206: Cluster for 12 OSDs	14:00 per day
OSD207	OSD_207_0	OS 207: Cluster for 12 OSDs	14:00 per day
OSD208	OSD_208_0	OS 208: Cluster for 12 OSDs	14:00 per day
OSD209	OSD_209_0	OS 209: Cluster for 12 OSDs	14:00 per day
OSD210	OSD_210_0	OS 210: Cluster for 12 OSDs	14:00 per day
OSD211	OSD_211_0	OS 211: Cluster for 12 OSDs	14:00 per day
OSD212	OSD_212_0	OS 212: Cluster for 12 OSDs	14:00 per day
OSD213	OSD_213_0	OS 213: Cluster for 12 OSDs	14:00 per day
OSD214	OSD_214_0	OS 214: Cluster for 12 OSDs	14:00 per day
OSD215	OSD_215_0	OS 215: Cluster for 12 OSDs	14:00 per day
OSD216	OSD_216_0	OS 216: Cluster for 12 OSDs	14:00 per day
OSD217	OSD_217_0	OS 217: Cluster for 12 OSDs	14:00 per day
OSD218	OSD_218_0	OS 218: Cluster for 12 OSDs	14:00 per day
OSD219	OSD_219_0	OS 219: Cluster for 12 OSDs	14:00 per day
OSD220	OSD_220_0	OS 220: Cluster for 12 OSDs	14:00 per day
OSD221	OSD_221_0	OS 221: Cluster for 12 OSDs	

GPU ID	H16-0283-64 GPU Configuration	H17-0283-64 GPU Configuration	H17 GPU ID	Comments
GPU0	CL2A, 3A, 3B	Power, 1A, 1B	GPU0, 1A	not used; GPU for H17-0283-64
GPU1	Power, 1A, 1B	CL2A, 3A, 3B	GPU1, 1A	not used; GPU for H17-0283-64
GPU2	CL2A, 3A, 3B	Power, 1A, 1B	GPU2, 1A	not used; GPU for H17-0283-64
GPU3	CL2A, 3A, 3B	Power, 1A, 1B	GPU3, 1A	not used; GPU for H17-0283-64
GPU4	CL2A, 3A, 3B	Power, 1A, 1B	GPU4, 1A	not used; GPU for H17-0283-64
GPU5	CL2A, 3A, 3B	Power, 1A, 1B	GPU5, 1A	not used; GPU for H17-0283-64
GPU6	CL2A, 3A, 3B	Power, 1A, 1B	GPU6, 1A	not used; GPU for H17-0283-64
GPU7	CL2A, 3A, 3B	Power, 1A, 1B	GPU7, 1A	not used; GPU for H17-0283-64
GPU8	CL2A, 3A, 3B	Power, 1A, 1B	GPU8, 1A	not used; GPU for H17-0283-64
GPU9	CL2A, 3A, 3B	Power, 1A, 1B	GPU9, 1A	not used; GPU for H17-0283-64
GPU10	CL2A, 3A, 3B	Power, 1A, 1B	GPU10, 1A	not used; GPU for H17-0283-64
GPU11	Power, 1A, 1B	CL2A, 3A, 3B	GPU11, 1A	not used; GPU for H17-0283-64
GPU12	CL2A, 3A, 3B	Power, 1A, 1B	GPU12, 1A	not used; GPU for H17-0283-64
GPU13	CL2A, 3A, 3B	Power, 1A, 1B	GPU13, 1A	not used; GPU for H17-0283-64
GPU14	CL2A, 3A, 3B	Power, 1A, 1B	GPU14, 1A	not used; GPU for H17-0283-64
GPU15	CL2A, 3A, 3B	Power, 1A, 1B	GPU15, 1A	not used; GPU for H17-0283-64
GPU16	CL2A, 3A, 3B	Power, 1A, 1B	GPU16, 1A	not used; GPU for H17-0283-64
GPU17	CL2A, 3A, 3B	Power, 1A, 1B	GPU17, 1A	not used; GPU for H17-0283-64
GPU18	CL2A, 3A, 3B	Power, 1A, 1B	GPU18, 1A	not used; GPU for H17-0283-64
GPU19	CL2A, 3A, 3B	Power, 1A, 1B	GPU19, 1A	not used; GPU for H17-0283-64
GPU20	CL2A, 3A, 3B	Power, 1A, 1B	GPU20, 1A	not used; GPU for H17-0283-64
GPU21	CL2A, 3A, 3B	Power, 1A, 1B	GPU21, 1A	not used; GPU for H17-0283-64
GPU22	CL2A, 3A, 3B	Power, 1A, 1B	GPU22, 1A	not used; GPU for H17-0283-64
GPU23	CL2A, 3A, 3B	Power, 1A, 1B	GPU23, 1A	not used; GPU for H17-0283-64
GPU24	CL2A, 3A, 3B	Power, 1A, 1B	GPU24, 1A	not used; GPU for H17-0283-64
GPU25	CL2A, 3A, 3B	Power, 1A, 1B	GPU25, 1A	not used; GPU for H17-0283-64
GPU26	CL2A, 3A, 3B	Power, 1A, 1B	GPU26, 1A	not used; GPU for H17-0283-64
GPU27	CL2A, 3A, 3B	Power, 1A, 1B	GPU27, 1A	not used; GPU for H17-0283-64
GPU28	CL2A, 3A, 3B	Power, 1A, 1B	GPU28, 1A	not used; GPU for H17-0283-64
GPU29	CL2A, 3A, 3B	Power, 1A, 1B	GPU29, 1A	not used; GPU for H17-0283-64
GPU30	CL2A, 3A, 3B	Power, 1A, 1B	GPU30, 1A	not used; GPU for H17-0283-64
GPU31	CL2A, 3A, 3B	Power, 1A, 1B	GPU31, 1A	not used; GPU for H17-0283-64
GPU32	CL2A, 3A, 3B	Power, 1A, 1B	GPU32, 1A	not used; GPU for H17-0283-64
GPU33	CL2A, 3A, 3B	Power, 1A, 1B	GPU33, 1A	not used; GPU for H17-0283-64
GPU34	CL2A, 3A, 3B	Power, 1A, 1B	GPU34, 1A	not used; GPU for H17-0283-64
GPU35	CL2A, 3A, 3B	Power, 1A, 1B	GPU35, 1A	not used; GPU for H17-0283-64
GPU36	CL2A, 3A, 3B	Power, 1A, 1B	GPU36, 1A	not used; GPU for H17-0283-64
GPU37	CL2A, 3A, 3B	Power, 1A, 1B	GPU37, 1A	not used; GPU for H17-0283-64
GPU38	CL2A, 3A, 3B	Power, 1A, 1B	GPU38, 1A	not used; GPU for H17-0283-64
GPU39	CL2A, 3A, 3B	Power, 1A, 1B	GPU39, 1A	not used; GPU for H17-0283-64
GPU40	CL2A, 3A, 3B	Power, 1A, 1B	GPU40, 1A	not used; GPU for H17-0283-64
GPU41	CL2A, 3A, 3B	Power, 1A, 1B	GPU41, 1A	not used; GPU for H17-0283-64
GPU42	CL2A, 3A, 3B	Power, 1A, 1B	GPU42, 1A	not used; GPU for H17-0283-64
GPU43	CL2A, 3A, 3B	Power, 1A, 1B	GPU43, 1A	not used; GPU for H17-0283-64
GPU44	CL2A, 3A, 3B	Power, 1A, 1B	GPU44, 1A	not used; GPU for H17-0283-64
GPU45	CL2A, 3A, 3B	Power, 1A, 1B	GPU45, 1A	not used; GPU for H17-0283-64
GPU46	CL2A, 3A, 3B	Power, 1A, 1B	GPU46, 1A	not used; GPU for H17-0283-64
GPU47	CL2A, 3A, 3B	Power, 1A, 1B	GPU47, 1A	not used; GPU for H17-0283-64
GPU48	CL2A, 3A, 3B	Power, 1A, 1B	GPU48, 1A	not used; GPU for H17-0283-64
GPU49	CL2A, 3A, 3B	Power, 1A, 1B	GPU49, 1A	not used; GPU for H17-0283-64
GPU50	CL2A, 3A, 3B	Power, 1A, 1B	GPU50, 1A	not used; GPU for H17-0283-64
GPU51	CL2A, 3A, 3B	Power, 1A, 1B	GPU51, 1A	not used; GPU for H17-0283-64
GPU52	CL2A, 3A, 3B	Power, 1A, 1B	GPU52, 1A	not used; GPU for H17-0283-64
GPU53	CL2A, 3A, 3B	Power, 1A, 1B	GPU53, 1A	not used; GPU for H17-0283-64
GPU54	CL2A, 3A, 3B	Power, 1A, 1B	GPU54, 1A	not used; GPU for H17-0283-64
GPU55	CL2A, 3A, 3B	Power, 1A, 1B	GPU55, 1A	not used; GPU for H17-0283-64
GPU56	CL2A, 3A, 3B	Power, 1A, 1B	GPU56, 1A	not used; GPU for H17-0283-64
GPU57	CL2A, 3A, 3B	Power, 1A, 1B	GPU57, 1A	not used; GPU for H17-0283-64
GPU58	CL2A, 3A, 3B	Power, 1A, 1B	GPU58, 1A	not used; GPU for H17-0283-64
GPU59	CL2A, 3A, 3B	Power, 1A, 1B	GPU59, 1A	not used; GPU for H17-0283-64
GPU60	CL2A, 3A, 3B	Power, 1A, 1B	GPU60, 1A	not used; GPU for H17-0283-64
GPU61	CL2A, 3A, 3B	Power, 1A, 1B	GPU61, 1A	not used; GPU for H17-0283-64
GPU62	CL2A, 3A, 3B	Power, 1A, 1B	GPU62, 1A	not used; GPU for H17-0283-64
GPU63	CL2A, 3A, 3B	Power, 1A, 1B	GPU63, 1A	not used; GPU for H17-0283-64
GPU64	CL2A, 3A, 3B	Power, 1A, 1B	GPU64, 1A	not used; GPU for H17-0283-64
GPU65	CL2A, 3A, 3B	Power, 1A, 1B	GPU65, 1A	not used; GPU for H17-0283-64
GPU66	CL2A, 3A, 3B	Power, 1A, 1B	GPU66, 1A	not used; GPU for H17-0283-64
GPU67	CL2A, 3A, 3B	Power, 1A, 1B	GPU67, 1A	not used; GPU for H17-0283-64
GPU68	CL2A, 3A, 3B	Power, 1A, 1B	GPU68, 1A	not used; GPU for H17-0283-64
GPU69	CL2A, 3A, 3B	Power, 1A, 1B	GPU69, 1A	not used; GPU for H17-0283-64
GPU70	CL2A, 3A, 3B	Power, 1A, 1B	GPU70, 1A	not used; GPU for H17-0283-64
GPU71	CL2A, 3A, 3B	Power, 1A, 1B	GPU71, 1A	not used; GPU for H17-0283-64
GPU72	CL2A, 3A, 3B	Power, 1A, 1B	GPU72, 1A	not used; GPU for H17-0283-64
GPU73	CL2A, 3A, 3B	Power, 1A, 1B	GPU73, 1A	not used; GPU for H17-0283-64
GPU74	CL2A, 3A, 3B	Power, 1A, 1B	GPU74, 1A	not used; GPU for H17-0283-64
GPU75	CL2A, 3A, 3B	Power, 1A, 1B	GPU75, 1A	not used; GPU for H17-0283-64
GPU76	CL2A, 3A, 3B	Power, 1A, 1B	GPU76, 1A	not used; GPU for H17-0283-64
GPU77	CL2A, 3A, 3B	Power, 1A, 1B	GPU77, 1A	not used; GPU for H17-0283-64
GPU78	CL2A, 3A, 3B	Power, 1A, 1B	GPU78, 1A	not used; GPU for H17-0283-64
GPU79	CL2A, 3A, 3B	Power, 1A, 1B	GPU79, 1A	not used; GPU for H17-0283-64
GPU80	CL2A, 3A, 3B	Power, 1A, 1B	GPU80, 1A	not used; GPU for H17-0283-64
GPU81	CL2A, 3A, 3B	Power, 1A, 1B	GPU81, 1A	not used; GPU for H17-0283-64
GPU82	CL2A, 3A, 3B	Power, 1A, 1B	GPU82, 1A	not used; GPU for H17-0283-64
GPU83	CL2A, 3A, 3B	Power, 1A, 1B	GPU83, 1A	not used; GPU for H17-0283-64
GPU84	CL2A, 3A, 3B	Power, 1A, 1B	GPU84, 1A	not used; GPU for H17-0283-64
GPU85	CL2A, 3A, 3B	Power, 1A, 1B	GPU85, 1A	not used; GPU for H17-0283-64
GPU86	CL2A, 3A, 3B	Power, 1A, 1B	GPU86, 1A	not used; GPU for H17-0283-64
GPU87	CL2A, 3A, 3B	Power, 1A, 1B	GPU87, 1A	not used; GPU for H17-0283-64
GPU88	CL2A, 3A, 3B	Power, 1A, 1B	GPU88, 1A	not used; GPU for H17-0283-64
GPU89	CL2A, 3A, 3B	Power, 1A, 1B	GPU89, 1A	not used; GPU for H17-0283-64
GPU90	CL2A, 3A, 3B	Power, 1A, 1B	GPU90, 1A	not used; GPU for H17-0283-64
GPU91	CL2A, 3A, 3B	Power, 1A, 1B	GPU91, 1A	not used; GPU for H17-0283-64
GPU92	CL2A, 3A, 3B	Power, 1A, 1B	GPU92, 1A	not used; GPU for H17-0283-64
GPU93	CL2A, 3A, 3B	Power, 1A, 1B	GPU93, 1A	not used; GPU for H17-0283-64
GPU94	CL2A, 3A, 3B	Power, 1A, 1B	GPU94, 1A	not used; GPU for H17-0283-64
GPU95	CL2A, 3A, 3B	Power, 1A, 1B	GPU95, 1A	not used; GPU for H17-0283-64
GPU96	CL2A, 3A, 3B	Power, 1A, 1B	GPU96, 1A	not used; GPU for H17-0283-64
GPU97	CL2A, 3A, 3B	Power, 1A, 1B	GPU97, 1A	not used; GPU for H17-0283-64
GPU98	CL2A, 3A, 3B	Power, 1A, 1B	GPU98, 1A	not used; GPU for H17-0283-64
GPU99	CL2A, 3A, 3B	Power, 1A, 1B	GPU99, 1A	not used; GPU for H17-0283-64
GPU100	CL2A, 3A, 3B	Power, 1A, 1B	GPU100, 1A	not used; GPU for H17-0283-64
GPU101	CL2A, 3A, 3B	Power, 1A, 1B	GPU101, 1A	not used; GPU for H17-0283-64
GPU102	CL2A, 3A, 3B	Power, 1A, 1B	GPU102, 1A	not used; GPU for H17-0283-64
GPU103	CL2A, 3A, 3B	Power, 1A, 1B	GPU103, 1A	not used; GPU for H17-0283-64
GPU104	CL2A, 3A, 3B	Power, 1A, 1B	GPU104, 1A	not used; GPU for H17-0283-64
GPU105	CL2A, 3A, 3B	Power, 1A, 1B	GPU105, 1A	not used; GPU for H17-0283-64
GPU106	CL2A, 3A, 3B	Power, 1A, 1B	GPU106, 1A	not used; GPU for H17-0283-64
GPU107	CL2A, 3A, 3B	Power, 1A, 1B	GPU107, 1A	not used; GPU for H17-0283-64
GPU108	CL2A, 3A, 3B	Power, 1A, 1B	GPU108, 1A	not used; GPU for H17-0283-64
GPU109	CL2A, 3A, 3B	Power, 1A, 1B	GPU109, 1A	not used; GPU for H17-0283-64
GPU110	CL2A, 3A, 3B	Power, 1A, 1B	GPU110, 1A	not used; GPU for H17-0283-64
GPU111	Power, 1A, 1B	CL2A, 3A, 3B	GPU111, 1A	not used; GPU for H17-0283-64
GPU112	CL2A, 3A, 3B	Power, 1A, 1B	GPU112, 1A	not used; GPU for H17-0283-64
GPU113	CL2A, 3A, 3B	Power, 1A, 1B	GPU113, 1A	not used; GPU for H17-0283-64
GPU114	CL2A, 3A, 3B	Power, 1A, 1B	GPU114, 1A	not used; GPU for H17-0283-64
GPU115	CL2A, 3A, 3B	Power, 1A, 1B	GPU115, 1A	not used; GPU for H17-0283-64
GPU116	CL2A, 3A, 3B	Power, 1A, 1B	GPU116, 1A	not used; GPU for H17-0283-64
GPU117	CL2A, 3A, 3B	Power, 1A, 1B	GPU117, 1A	not used; GPU for H17-0283-64
GPU118	CL2A, 3A, 3B	Power, 1A, 1B	GPU118, 1A	not used; GPU for H17-0283-64
GPU119	CL2A, 3A, 3B	Power, 1A, 1B	GPU119, 1A	not used; GPU for H17-0283-64
GPU120	CL2A, 3A, 3B	Power, 1A, 1B	GPU120, 1A	not used; GPU for H17-0283-64
GPU121	CL2A, 3A, 3B	Power, 1A, 1B	GPU121, 1A	not used; GPU for H17-0283-64
GPU122	CL2A, 3A, 3B	Power, 1A, 1B	GPU122, 1A	not used; GPU for H17-0283-64
GPU123	CL2A, 3A, 3B	Power, 1A, 1B	GPU123, 1A	not used; GPU for H17-0283-64
GPU124	CL2A, 3A, 3B	Power, 1A, 1B	GPU124, 1A	not used; GPU for H17-0283-64
GPU125	CL2A, 3A, 3B	Power, 1A, 1B	GPU125, 1A	not used; GPU for H17-0283-64
GPU126	CL2A, 3A, 3B	Power, 1A, 1B	GPU126, 1A	not used; GPU for H17-0283-64
GPU127	CL2A, 3A, 3B	Power, 1A, 1B	GPU127, 1A	not used; GPU for H17-0283-64
GPU128	CL2A, 3A, 3B	Power, 1A, 1B	GPU128, 1A	not used; GPU for H17-0283-64
GPU129	CL2A, 3A, 3B	Power, 1A, 1B	GPU129, 1A	not used; GPU for H17-0283-64
GPU130	CL2A, 3A, 3B	Power, 1A, 1B	GPU130, 1A	not used; GPU for H17-0283-64
GPU131	CL2A, 3A, 3B	Power, 1A, 1B	GPU131, 1A	not used; GPU for H17-0283-64
GPU132	CL2A, 3A, 3B	Power, 1A, 1B	GPU132, 1A	not used; GPU for H17-0283-64
GPU133	CL2A, 3A, 3B	Power, 1A, 1B	GPU133, 1A	not used; GPU for H17-0283-64
GPU134	CL2A, 3A, 3B	Power, 1A, 1B	GPU134, 1A	not used; GPU for H17-0283-64
GPU135	CL2A, 3A, 3B	Power, 1A, 1B	GPU135, 1A	not used; GPU for H17-0283-64
GPU136	CL2A, 3A, 3B	Power, 1A, 1B	GPU136, 1A	not used; GPU for H17-0283-64
GPU137	CL2A, 3A, 3B	Power, 1A, 1B	GPU137, 1A	not used; GPU for H17-0283-64
GPU138	CL2A, 3A, 3B	Power, 1A, 1B	GPU138, 1A	not used; GPU for H17-0283-64
GPU139	CL2A, 3A, 3B	Power, 1A, 1B	GPU139, 1A	not used; GPU for H17-0283-64
GPU140	CL2A, 3A, 3B	Power, 1A, 1B	GPU140, 1A	not used; GPU for H17-0283-64
GPU141	CL2A, 3A, 3B	Power, 1A, 1B	GPU141, 1A	not used; GPU for H17-0283-64
GPU142	CL2A, 3A, 3B	Power, 1A, 1B	GPU142, 1A	not used; GPU for H17-0283-64
GPU143	CL2A, 3A, 3B	Power, 1A, 1B	GPU143, 1A	not used; GPU for H17-0283-64
GPU144	CL2A, 3A, 3B	Power, 1A, 1B	GPU144, 1A	not used; GPU for H17-0283-64
GPU145	CL2A, 3A, 3B	Power, 1A, 1B	GPU145, 1A	not used; GPU for H17-0283-64
GPU146	CL2A, 3A, 3B	Power, 1A, 1B	GPU146, 1A	not used; GPU for H17-0283-64
GPU147	CL2A, 3A, 3B	Power, 1A, 1B	GPU147, 1A	not used; GPU for H17-0283-64
GPU148	CL2A, 3A, 3B	Power, 1A, 1B	GPU148, 1A	not used; GPU for H17-0283-64
GPU149	CL2A, 3A, 3B	Power, 1A, 1B	GPU149, 1A	not used; GPU for H17-0283-64
GPU150	CL2A, 3A, 3B	Power, 1A, 1B	GPU150, 1A	not used; GPU for H17-0283-64
GPU151	CL2A, 3A, 3B	Power, 1A, 1B	GPU151, 1A	not used; GPU for H17-0283-64
GPU152	CL2A, 3A, 3B	Power, 1A, 1B	GPU152, 1A	not used; GPU for H17-0283-64
GPU153	CL2A, 3A, 3B	Power, 1A, 1B	GPU153, 1A	not used; GPU for H17-0283-64
GPU154	CL2A, 3A, 3B	Power, 1A, 1B	GPU154, 1A	not used; GPU for H17-0283-64
GPU155	CL2A, 3A, 3B	Power, 1A, 1B	GPU155, 1A	not used; GPU for H17-0283-64
GPU156	CL2A, 3A, 3B	Power, 1A, 1B	GPU156, 1A	not used; GPU for H17-0283-64
GPU157	CL2A, 3A, 3B	Power, 1A, 1B	GPU157, 1A	not used; GPU for H17-0283-64
GPU158	CL2A, 3A, 3B	Power, 1A, 1B	GPU158, 1A	not used; GPU for H17-0283-64
GPU159	CL2A, 3A, 3B	Power, 1A, 1B	GPU159, 1A	not used; GPU for H17-0283-64
GPU160	CL2A, 3A, 3B	Power, 1A, 1B	GPU160, 1A	not used; GPU for H17-0283-64
GPU161	CL2A, 3A, 3B	Power, 1A, 1B	GPU161, 1A	not used; GPU for H17-0283-64
GPU162	CL2A, 3A, 3B	Power, 1		



Schematic Page List

Hide

U68_

069_****

070_GPU_PCI-EXPRESS

071_GPU_MEMORY INTERFACE

072_VGA_GDDR5_(Bit0~63)

073_VGA_GDDR5_(NA)

074_VGA_nVIDIA_N16V/S_DISPL
AY

075_GPU_GPIO_THERM

076_GPU_XTAL/STRAPPING

077_GPU_PWG/GND

078_GPU_Power_Sel

079_****

080_PW_KABY LAKE-U(1)

081_PW_KABY LAKE-U(2)

082_PW_***

083_PW_+1.0VSUS/1.8VSUS(UP
9002)

084_PW_

085_PW_***

086_PW_1.2V/+VTT/+2.5V(UP900
2)

Global Search

057

058

060

061

065

070

071

072

074

075

076

Toggle FullScreen

Pre Page

Next Page

MoveTo

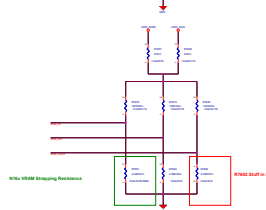
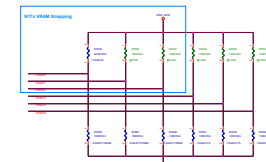
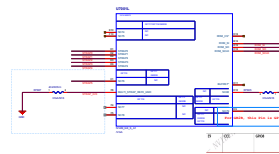


Table 4- GPU/GPU Recommended Parameters									
Parameter	Value	Unit	Min	Max	Typ	Test Condition	Notes	Ref	Ref
GPU/GPU	1.000	V	0.900	1.100	1.000	At 100°C	GPU/GPU	1.000	1.000
GPU/GPU	1.000	V	0.900	1.100	1.000	At 100°C	GPU/GPU	1.000	1.000
GPU/GPU	1.000	V	0.900	1.100	1.000	At 100°C	GPU/GPU	1.000	1.000
GPU/GPU	1.000	V	0.900	1.100	1.000	At 100°C	GPU/GPU	1.000	1.000
GPU/GPU	1.000	V	0.900	1.100	1.000	At 100°C	GPU/GPU	1.000	1.000
GPU/GPU	1.000	V	0.900	1.100	1.000	At 100°C	GPU/GPU	1.000	1.000
GPU/GPU	1.000	V	0.900	1.100	1.000	At 100°C	GPU/GPU	1.000	1.000
GPU/GPU	1.000	V	0.900	1.100	1.000	At 100°C	GPU/GPU	1.000	1.000
GPU/GPU	1.000	V	0.900	1.100	1.000	At 100°C	GPU/GPU	1.000	1.000
GPU/GPU	1.000	V	0.900	1.100	1.000	At 100°C	GPU/GPU	1.000	1.000

Table 5-3. RAMCFG									
STRAP	STRAP	STRAP	STRAP	STRAP	STRAP	STRAP	STRAP	STRAP	STRAP
STRAP0	STRAP0	STRAP0	STRAP0	STRAP0	STRAP0	STRAP0	STRAP0	STRAP0	STRAP0
STRAP1	STRAP1	STRAP1	STRAP1	STRAP1	STRAP1	STRAP1	STRAP1	STRAP1	STRAP1
STRAP2	STRAP2	STRAP2	STRAP2	STRAP2	STRAP2	STRAP2	STRAP2	STRAP2	STRAP2
STRAP3	STRAP3	STRAP3	STRAP3	STRAP3	STRAP3	STRAP3	STRAP3	STRAP3	STRAP3
STRAP4	STRAP4	STRAP4	STRAP4	STRAP4	STRAP4	STRAP4	STRAP4	STRAP4	STRAP4
STRAP5	STRAP5	STRAP5	STRAP5	STRAP5	STRAP5	STRAP5	STRAP5	STRAP5	STRAP5
STRAP6	STRAP6	STRAP6	STRAP6	STRAP6	STRAP6	STRAP6	STRAP6	STRAP6	STRAP6
STRAP7	STRAP7	STRAP7	STRAP7	STRAP7	STRAP7	STRAP7	STRAP7	STRAP7	STRAP7
STRAP8	STRAP8	STRAP8	STRAP8	STRAP8	STRAP8	STRAP8	STRAP8	STRAP8	STRAP8

Table 15-2. Resistance Mapping to Hex Values									
Resistor Values	Pull-Up to VY1_MAIN	Pull-Down to GND	Resistor Values	Pull-Up to VY1_MAIN	Pull-Down to GND	Resistor Values	Pull-Up to VY1_MAIN	Pull-Down to GND	Resistor Values
4.99 kΩ	1000	0000	10.0 kΩ	1001	0001	15.0 kΩ	1010	0010	20.0 kΩ
10.0 kΩ	1001	0001	15.0 kΩ	1010	0010	20.0 kΩ	1011	0011	24.9 kΩ
15.0 kΩ	1010	0010	20.0 kΩ	1011	0011	24.9 kΩ	1100	0100	30.1 kΩ
20.0 kΩ	1011	0011	24.9 kΩ	1100	0100	30.1 kΩ	1101	0101	34.8 kΩ
24.9 kΩ	1100	0100	30.1 kΩ	1101	0101	34.8 kΩ	1110	0110	45.3 kΩ
30.1 kΩ	1101	0101	34.8 kΩ	1110	0110	45.3 kΩ	1111	0111	Q255

Table 15-2. Resistance Mapping to Hex Values									
Resistor Values	Pull-Up to VY1_MAIN	Pull-Down to GND	Resistor Values	Pull-Up to VY1_MAIN	Pull-Down to GND	Resistor Values	Pull-Up to VY1_MAIN	Pull-Down to GND	Resistor Values
4.99 kΩ	1000	0000	10.0 kΩ	1001	0001	15.0 kΩ	1010	0010	20.0 kΩ
10.0 kΩ	1001	0001	15.0 kΩ	1010	0010	20.0 kΩ	1011	0011	24.9 kΩ
15.0 kΩ	1010	0010	20.0 kΩ	1011	0011	24.9 kΩ	1100	0100	30.1 kΩ
20.0 kΩ	1011	0011	24.9 kΩ	1100	0100	30.1 kΩ	1101	0101	34.8 kΩ
24.9 kΩ	1100	0100	30.1 kΩ	1101	0101	34.8 kΩ	1110	0110	45.3 kΩ
30.1 kΩ	1101	0101	34.8 kΩ	1110	0110	45.3 kΩ	1111	0111	Q255

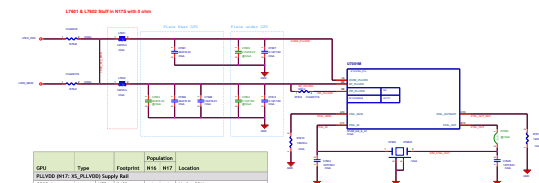


Table 15-2. Resistance Mapping to Hex Values									
Resistor Values	Pull-Up to VY1_MAIN	Pull-Down to GND	Resistor Values	Pull-Up to VY1_MAIN	Pull-Down to GND	Resistor Values	Pull-Up to VY1_MAIN	Pull-Down to GND	Resistor Values
4.99 kΩ	1000	0000	10.0 kΩ	1001	0001	15.0 kΩ	1010	0010	20.0 kΩ
10.0 kΩ	1001	0001	15.0 kΩ	1010	0010	20.0 kΩ	1011	0011	24.9 kΩ
15.0 kΩ	1010	0010	20.0 kΩ	1011	0011	24.9 kΩ	1100	0100	30.1 kΩ
20.0 kΩ	1011	0011	24.9 kΩ	1100	0100	30.1 kΩ	1101	0101	34.8 kΩ
24.9 kΩ	1100	0100	30.1 kΩ	1101	0101	34.8 kΩ	1110	0110	45.3 kΩ
30.1 kΩ	1101	0101	34.8 kΩ	1110	0110	45.3 kΩ	1111	0111	Q255

Table 15-2. Resistance Mapping to Hex Values									
Resistor Values	Pull-Up to VY1_MAIN	Pull-Down to GND	Resistor Values	Pull-Up to VY1_MAIN	Pull-Down to GND	Resistor Values	Pull-Up to VY1_MAIN	Pull-Down to GND	Resistor Values
4.99 kΩ	1000	0000	10.0 kΩ	1001	0001	15.0 kΩ	1010	0010	20.0 kΩ
10.0 kΩ	1001	0001	15.0 kΩ	1010	0010	20.0 kΩ	1011	0011	24.9 kΩ
15.0 kΩ	1010	0010	20.0 kΩ	1011	0011	24.9 kΩ	1100	0100	30.1 kΩ
20.0 kΩ	1011	0011	24.9 kΩ	1100	0100	30.1 kΩ	1101	0101	34.8 kΩ
24.9 kΩ	1100	0100	30.1 kΩ	1101	0101	34.8 kΩ	1110	0110	45.3 kΩ
30.1 kΩ	1101	0101	34.8 kΩ	1110	0110	45.3 kΩ	1111	0111	Q255



Schematic Page List

Hide

- AY
- 075_GPU_GPIO_THERM
- 076_GPU_XTAL/STRAPPING
- 077_GPU_PWG/GND
- 078_GPU_Power_Sel
- 079_*****
- 080_PW_KABY LAKE-U(1)
- 081_PW_KABY LAKE-U(2)
- 082_PW_***
- 083_PW_+1.0VSUS/1.8VSUS(UP 9002)
- 084_PW_
- 085_PW_***
- 086_PW_1.2V/+VTT/+2.5V(UP900 2)
- 087_PW_+3VADSW/+5VSUS(UP9 003)
- 088_PW_LOAD SWITCH
- 089.PW_CHARGER
- 090_PW_PROTECTION
- 091_PW_DGPU_2PHASE(uP902 4)

Global Search

077

078

080

081

083

086

087

088

089

090

091

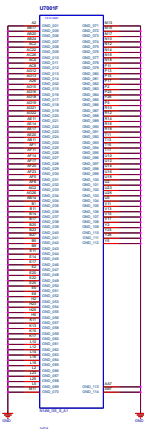
Toggle FullScreen

Pre Page

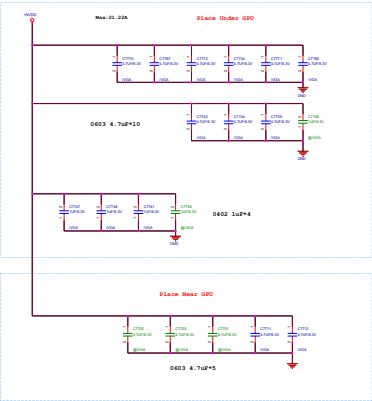
Next Page

MoveTo

NVDD GROUND



NVDD POWER AND DECOUPLING



330uF & 22uF reserved in NVDD Power page

For N175 @3.50Hz

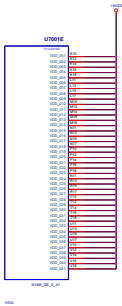
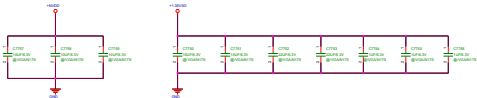


Table 3. NVDD and NVDD5 Decoupling and Filtering

GPU	Capacitor Type	Footprint	Population N16 (N17)	Location
NVDD Supply Net				
GR32-64, GR32-64	4.7 uF	X65	0603	8 Under GPU
	1 uF	X65	0402	4 Under GPU
	47 uF	X58	0805	1 Near GPU
	10 uF	X78	0805	1 Near GPU
	22 uF	X58	0805	1 Near GPU
	4.7 uF	X58	0805	4 Near GPU
	330 uF	X65	7343	1 Near GPU
NVDD5 Supply Net				
GR32-64 Only	4.7 uF	X65	0603	N/A 4 Under GPU
	1 uF	X65	0402	N/A 2 Under GPU
	10 uF	X65	0805	N/A 7 Near GPU
	22 uF	X65	0805LP	N/A 1 Near GPU
	330 uF	POS	7343	N/A 1 Near GPU

In the case of a merged rail configuration with a single VDD core power rail, add the NVDD5 decoupling capacitors onto the NVDD supply.

Schematic Page List

Hide

AY
075_GPU_GPIO_THERM
076_GPU_XTAL/STRAPPING
077_GPU_PWG/GND
078_GPU_Power_Sel
079_*****
080_PW_KABY LAKE-U(1)
081_PW_KABY LAKE-U(2)
082_PW_***
083_PW_+1.0VSUS/1.8VSUS(UP
9002)
084_PW_
085_PW_***
086_PW_1.2V/+VTT/+2.5V(UP900
2)
087_PW_+3VADSW/+5VSUS(UP9
003)
088_PW_LOAD SWITCH
089.PW_CHARGER
090_PW_PROTECTION
091_PW_DGPU_2PHASE(uP902
4)

Global Search

077

078

080

081

083

086

087

088

089

090

091

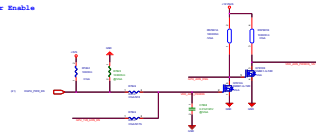
Toggle FullScreen

Pre Page

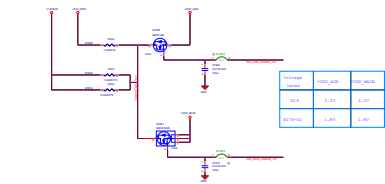
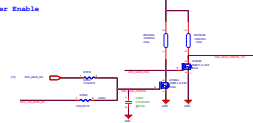
Next Page

MoveTo

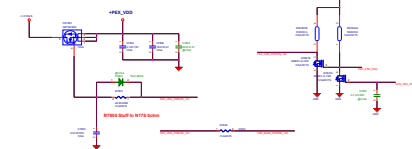
VDD_A0W Power Enable



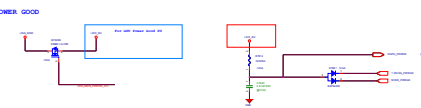
VDD_ML1W Power Enable



PEX_VDD Power Enable



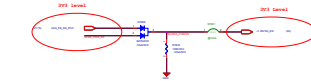
GPU POWER GOOD



SVDD Power Enable



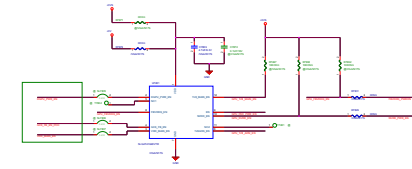
FVDDQ Power Enable



teknisi-indonesia

N17S GPU Sequence Solution

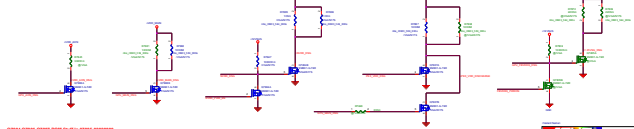
Input Signal for N17S



Output Signal for N17S(3.3V)

1. N17S_VDD_3.3V
2. N17S_VDD_3.3V
3. N17S_VDD_3.3V

GPU Power Discharge



GPU POWER GOOD



Schematic Page List

Hide

AY
075_GPU_GPIO_THERM
076_GPU_XTAL/STRAPPING
077_GPU_PWG/GND
078_GPU_Power_Sel
079_*****
080_PW_KABY LAKE-U(1)
081_PW_KABY LAKE-U(2)
082_PW_***
083_PW_+1.0VSUS/1.8VSUS(UP
9002)
084_PW_
085_PW_***
086_PW_1.2V/+VTT/+2.5V(UP900
2)
087_PW_+3VADSW/+5VSUS(UP9
003)
088_PW_LOAD SWITCH
089.PW_CHARGER
090_PW_PROTECTION
091_PW_DGPU_2PHASE(uP902
4)

Global Search

077

078

080

081

083

086

087

088

089

090

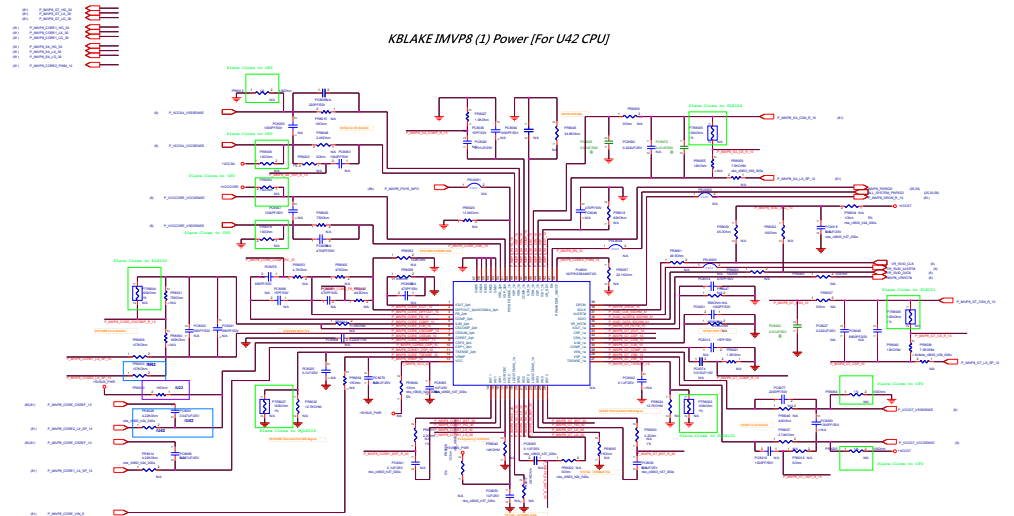
091

Toggle FullScreen

Pre Page

Next Page

MoveTo



08011 0 10021300003030 N/A 2.2 100213282003030 N/A



Schematic Page List

Hide

AY
075_GPU_GPIO_THERM
076_GPU_XTAL/STRAPPING
077_GPU_PWG/GND
078_GPU_Power_Sel
079_*****
080_PW_KABY LAKE-U(1)
081_PW_KABY LAKE-U(2)
082_PW_***
083_PW_+1.0VSUS/1.8VSUS(UP
9002)
084_PW_
085_PW_***
086_PW_1.2V/+VTT/+2.5V(UP900
2)
087_PW_+3VADSW/+5VSUS(UP9
003)
088_PW_LOAD SWITCH
089.PW_CHARGER
090_PW_PROTECTION
091_PW_DGPU_2PHASE(uP902
4)

Global Search

077

078

080

081

083

086

087

088

089

090

091

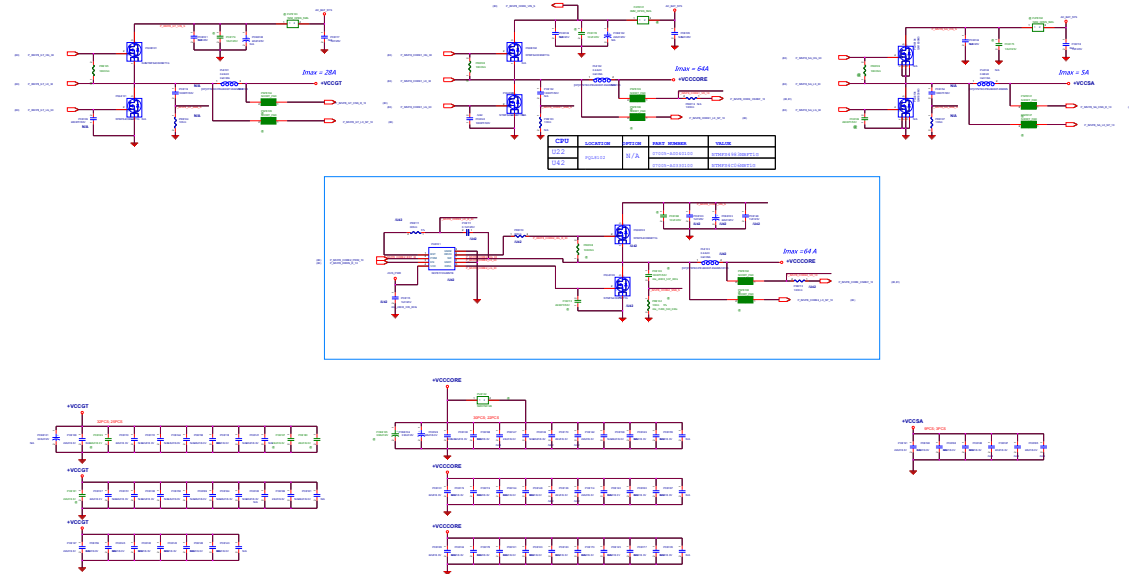
Toggle FullScreen

Pre Page

Next Page

MoveTo

Kaby Lake-U IMVP8 Power (2) [For CPU]



Hide

AY

075_GPU_GPIO_THERM

076_GPU_XTAL/STRAPPING

077_GPU_PWG/GND

078_GPU_Power_Sel

079_*****

080_PW_KABY LAKE-U(1)

081_PW_KABY LAKE-U(2)

082_PW_***

083_PW_+1.0VSUS/1.8VSUS(UP
9002)

084_PW_

085_PW_***

086_PW_1.2V/+VTT/+2.5V(UP900
2)

087_PW_+3VADSW/+5VSUS(UP9
003)

088_PW_LOAD SWITCH

089.PW_CHARGER

090_PW_PROTECTION

091_PW_DGPU_2PHASE(uP902
4)

077

078

080

081

083

086

087

088

089

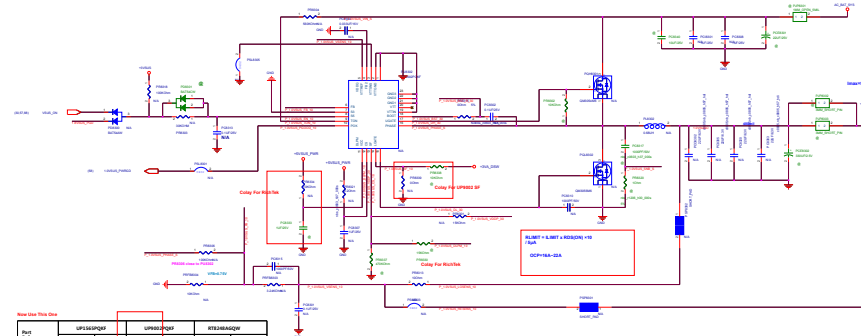
090

091

Pre Page

Next Page

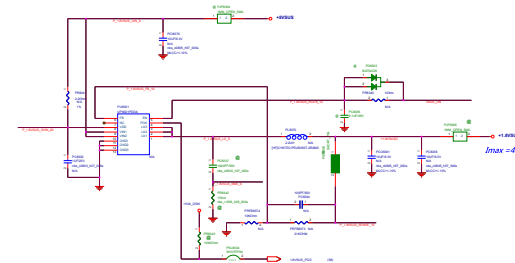
MoveTo



teknisi-indonesia

Part Reference	UP150PMH		UP180PMH		W220MAGW	
	MUCC	V-CHP	MUCC	V-CHP	MUCC	V-CHP
0000106	2.3ohm	2.3ohm	10ohm	10ohm	⊗	⊗
0000103	1uF	1uF	⊗	⊗	5ohm	5ohm
0000107	⊗	⊗	⊗	⊗	300ohm	300ohm
0000104	820ohm	820ohm	980ohm	980ohm	82ohm	82ohm
0000106	170ohm	170ohm	100ohm	⊗	170ohm	⊗
0000105	800uF	800uF	1000uF	⊗	800uF	⊗
0000101	150ohm	150ohm	150ohm	150ohm	⊗	⊗
0000100	⊗	⊗	⊗	⊗	150ohm	150ohm

+1.8VSUS [For PCH]



Schematic Page List

Hide

Global Search

077

078

080

081

083

086

087

088

089

090

091

Toggle FullScreen

Pre Page

Next Page

MoveTo

--	--

AY

075_GPU_GPIO_THERM

076_GPU_XTAL/STRAPPING

077_GPU_PWG/GND

078_GPU_Power_Sel

079_*****

080_PW_KABY LAKE-U(1)

081_PW_KABY LAKE-U(2)

082_PW_***

083_PW_+1.0VSUS/1.8VSUS(UP
9002)

084_PW_

085_PW_***

086_PW_1.2V/+VTT/+2.5V(UP900
2)

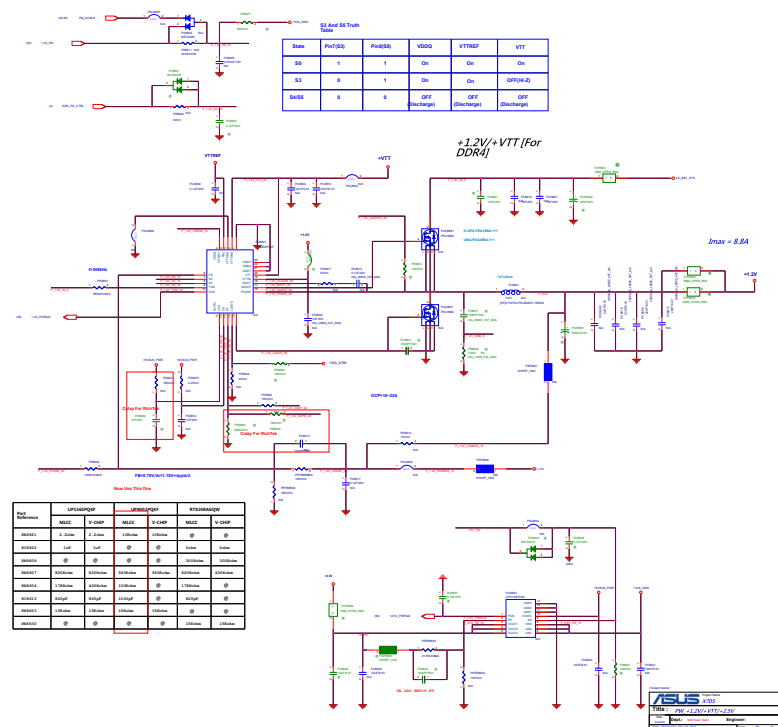
087_PW_+3VADSW/+5VSUS(UP9
003)

088_PW_LOAD SWITCH

089.PW CHARGER

090_PW_PROTECTION

091_PW_DGPU_2PHASE(uP902
4)



Hide

AY
075_GPU_GPIO_THERM
076_GPU_XTAL/STRAPPING
077_GPU_PWG/GND
078_GPU_Power_Sel
079_*****
080_PW_KABY LAKE-U(1)
081_PW_KABY LAKE-U(2)
082_PW_***
083_PW_+1.0VSUS/1.8VSUS(UP
9002)
084_PW_
085_PW_***
086_PW_1.2V/+VTT/+2.5V(UP900
2)
087_PW_+3VADSW/+5VSUS(UP9
003)
088_PW_LOAD SWITCH
089.PW_CHARGER
090_PW_PROTECTION
091_PW_DGPU_2PHASE(uP902
4)

077

078

080

081

083

086

087

088

089

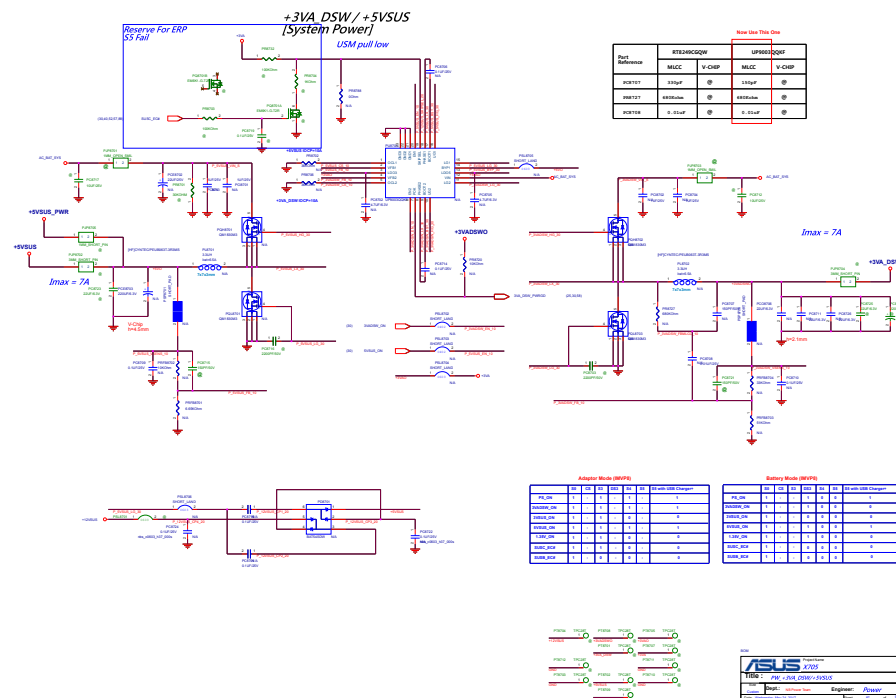
090

091

Pre Page

Next Page

MoveTo



Schematic Page List

Hide

AY
075_GPU_GPIO_THERM
076_GPU_XTAL/STRAPPING
077_GPU_PWG/GND
078_GPU_Power_Sel
079_*****
080_PW_KABY LAKE-U(1)
081_PW_KABY LAKE-U(2)
082_PW_***
083_PW_+1.0VSUS/1.8VSUS(UP
9002)
084_PW_
085_PW_***
086_PW_1.2V/+VTT/+2.5V(UP900
2)
087_PW_+3VADSW/+5VSUS(UP9
003)
088_PW_LOAD SWITCH
089.PW_CHARGER
090_PW_PROTECTION
091_PW_DGPU_2PHASE(uP902
4)

Global Search

077

078

080

081

083

086

087

088

089

090

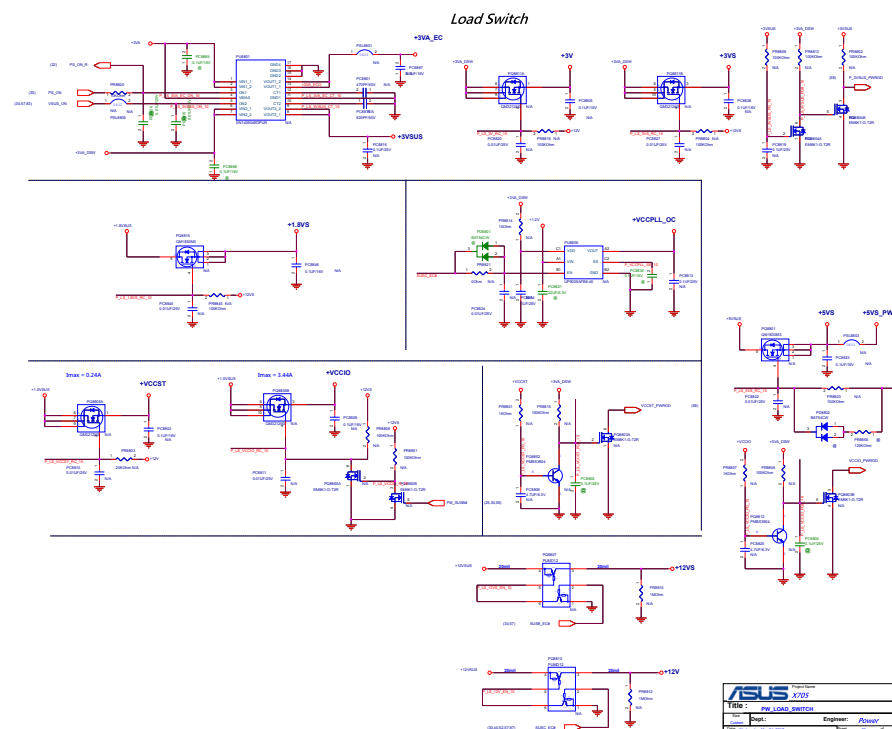
091

Toggle FullScreen

Pre Page

Next Page

MoveTo



Hide

AY

075_GPU_GPIO_THERM

076_GPU_XTAL/STRAPPING

077_GPU_PWG/GND

078_GPU_Power_Sel

079_*****

080_PW_KABY LAKE-U(1)

081_PW_KABY LAKE-U(2)

082_PW_***

083_PW_+1.0VSUS/1.8VSUS(UP9002)

084_PW_

085_PW_***

086_PW_1.2V/+VTT/+2.5V(UP9002)

087_PW_+3VADSW/+5VSUS(UP9003)

088_PW_LOAD SWITCH

089.PW_CHARGER

090_PW_PROTECTION

091_PW_DGPU_2PHASE(uP9024)

077

078

080

081

083

086

087

088

089

090

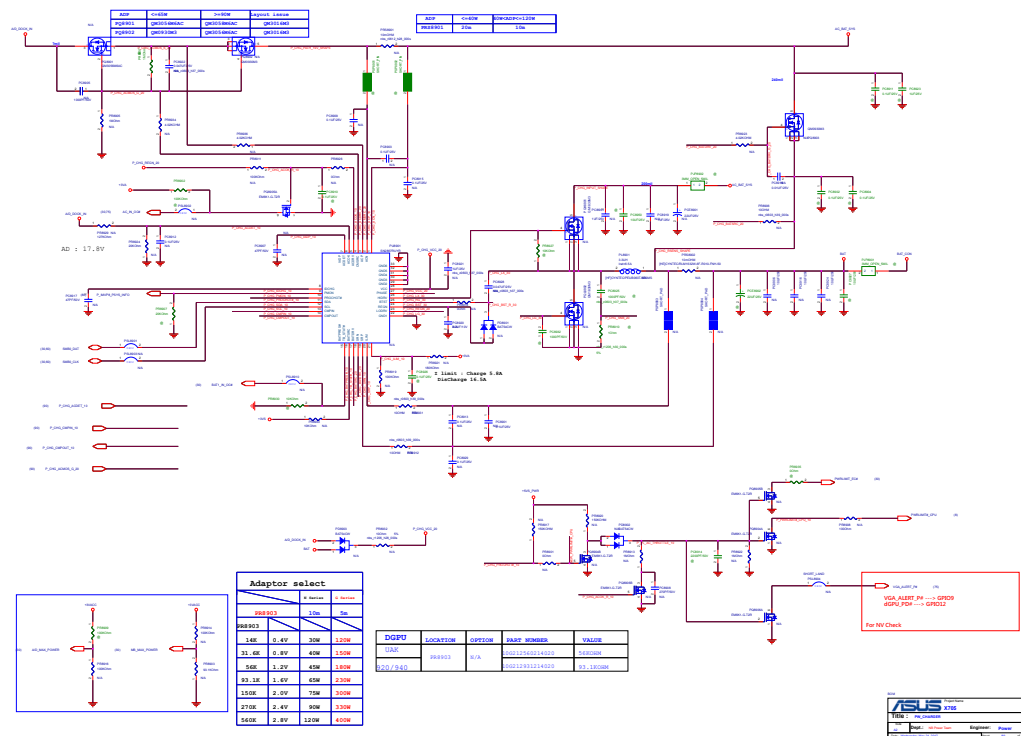
091

Toggle FullScreen

Pre Page

Next Page

MoveTo



Schematic Page List

Hide

AY
075_GPU_GPIO_THERM
076_GPU_XTAL/STRAPPING
077_GPU_PWG/GND
078_GPU_Power_Sel
079_****
080_PW_KABY LAKE-U(1)
081_PW_KABY LAKE-U(2)
082_PW_***
083_PW_+1.0VSUS/1.8VSUS(UP
9002)
084_PW_
085_PW_***
086_PW_1.2V/+VTT/+2.5V(UP900
2)
087_PW_+3VADSW/+5VSUS(UP9
003)
088_PW_LOAD SWITCH
089.PW_CHARGER
090_PW_PROTECTION
091_PW_DGPU_2PHASE(uP902
4)

Global Search

077

078

080

081

083

086

087

088

089

090

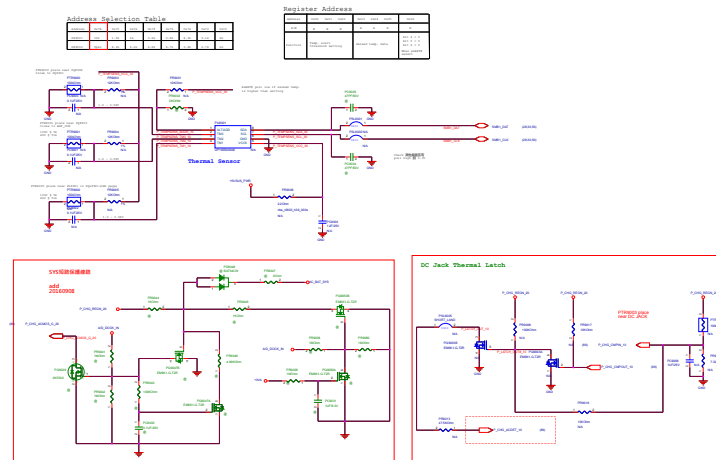
091

Toggle FullScreen

Pre Page

Next Page

MoveTo

www.teknisi-indonesia.com

Schematic Page List

Hide

AY

075_GPU_GPIO_THERM

076_GPU_XTAL/STRAPPING

077_GPU_PWG/GND

078_GPU_Power_Sel

079_*****

080_PW_KABY LAKE-U(1)

081_PW_KABY LAKE-U(2)

082_PW_***

083_PW_+1.0VSUS/1.8VSUS(UP
9002)

084_PW_

085_PW_***

086_PW_1.2V/+VTT/+2.5V(UP900
2)

087_PW_+3VADSW/+5VSUS(UP9
003)

088_PW_LOAD SWITCH

089.PW_CHARGER

090_PW_PROTECTION

091_PW_DGPU_2PHASE(uP902
4)

Global Search

077

078

080

081

083

086

087

088

089

090

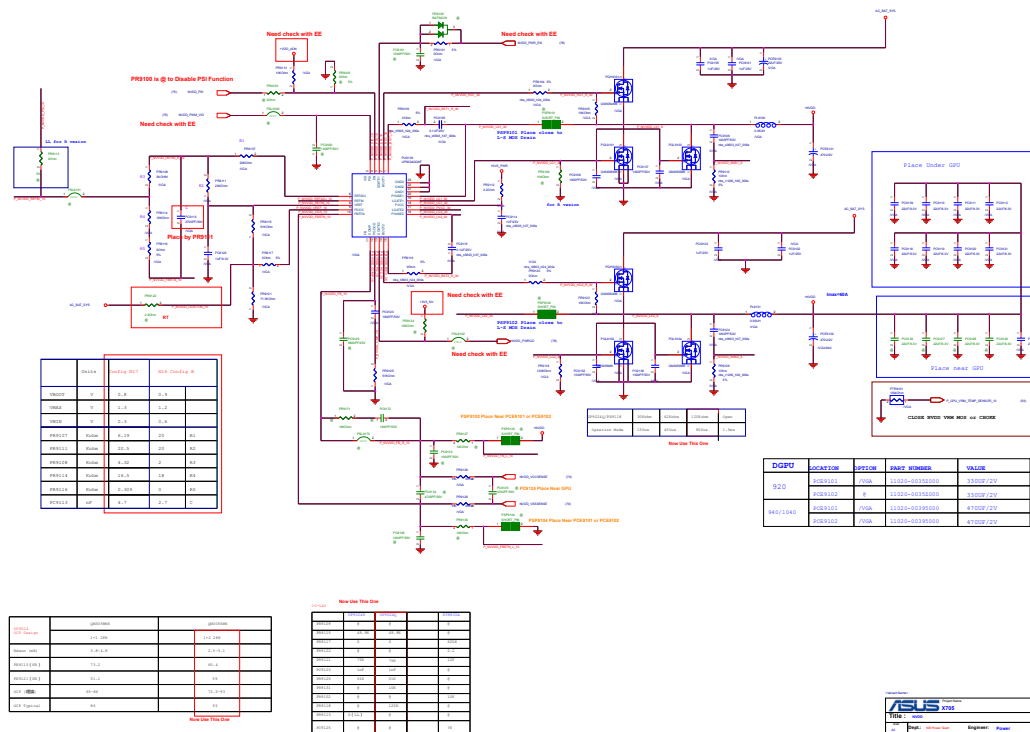
091

Toggle FullScreen

Pre Page

Next Page

MoveTo



Schematic Page List

Hide

U85_PW_+1.0VSUS/1.8VSUS(OP
9002)

084_PW_

085_PW_***

086_PW_1.2V/+VTT/+2.5V(UP900
2)

087_PW_+3VADSW/+5VSUS(UP9
003)

088 PW LOAD SWITCH

089.PW_CHARGER

090 PW PROTECTION

091_PW_DGPU_2PHASE(uP902
4)

092_PW_

093_PW_1.35VSG

094_PW_

095_PW_***

096_PW_***

097 PW

098_PW_***

099_PW_FLOW CHART

100_Power On Timing--AC mode

Global Search

093

099

100

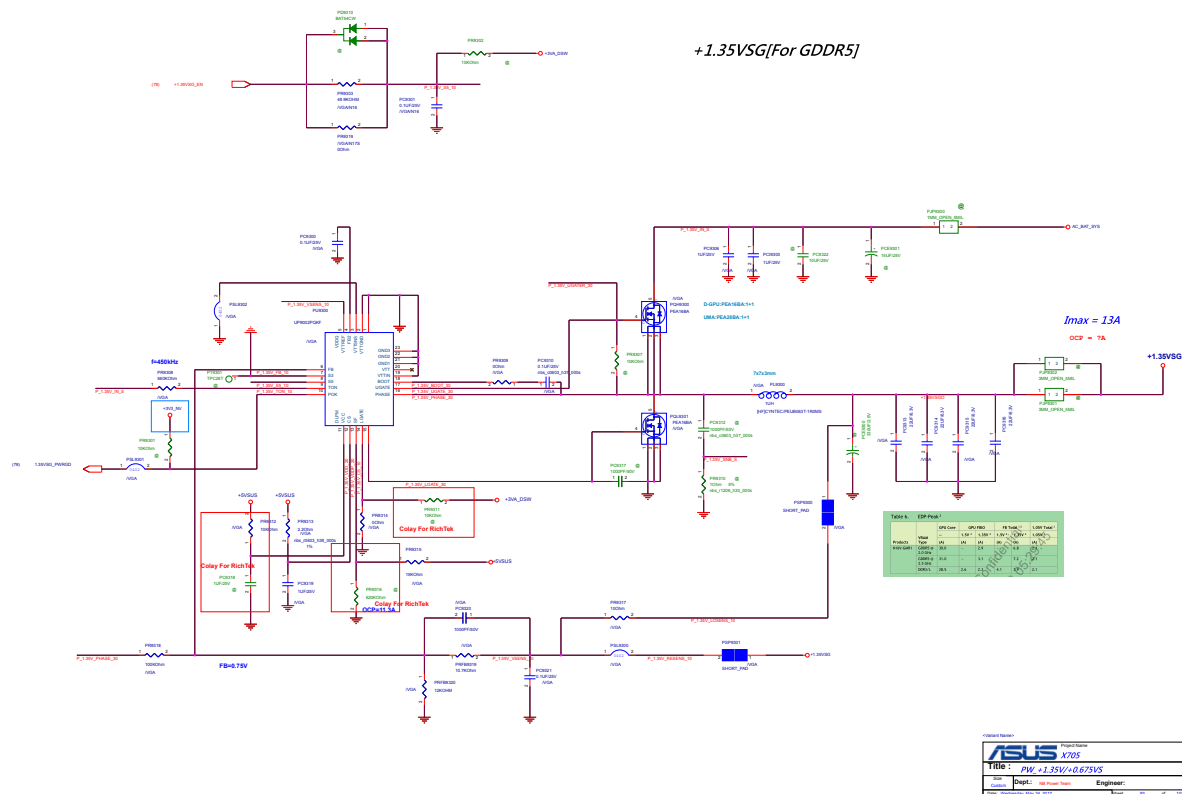
101

Toggle FullScreen

Pre Page

Next Page

MoveTo



Schematic Page List

Hide

083_PW_+1.0VSUS/1.8VSUS(UP
9002)
084_PW_
085_PW_***
086_PW_1.2V/+VTT/+2.5V(UP900
2)
087_PW_+3VADSW/+5VSUS(UP9
003)
088_PW_LOAD SWITCH
089.PW_CHARGER
090_PW_PROTECTION
091_PW_DGPU_2PHASE(uP902
4)

092_PW_
093_PW_1.35VSG
094_PW_
095_PW_***
096_PW_***
097_PW_
098_PW_***
099_PW_FLOW CHART
100_Power On Timing--AC mode

Global Search

093

099

100

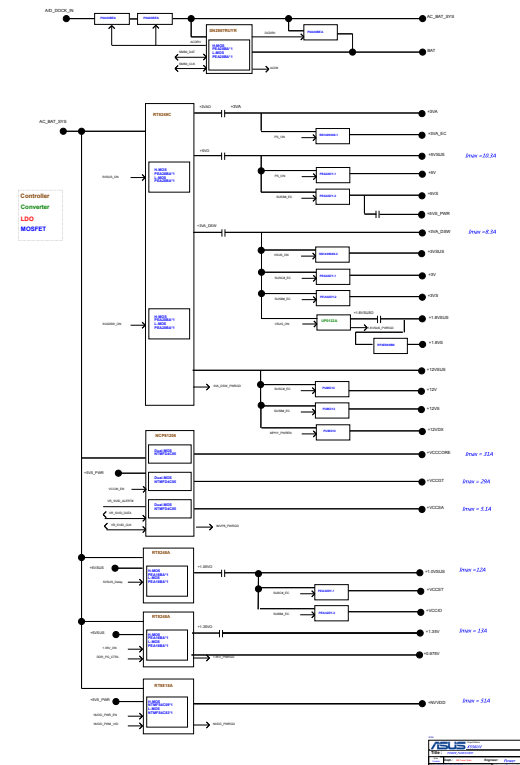
101

Toggle FullScreen

Pre Page

Next Page

MoveTo



Schematic Page List

Hide

083_PW_+1.0VSUS/1.8VSUS(UP
9002)

084_PW_

085_PW_***

086_PW_1.2V/+VTT/+2.5V(UP900
2)087_PW_+3VADSW/+5VSUS(UP9
003)

088_PW_LOAD SWITCH

089.PW_CHARGER

090_PW_PROTECTION

091_PW_DGPU_2PHASE(uP902
4)

092_PW_

093_PW_1.35VSG

094_PW_

095_PW_***

096_PW_***

097_PW_

098_PW_***

099_PW_FLOW CHART

100_Power On Timing--AC mode

Global Search

093

099

100

101

Toggle FullScreen

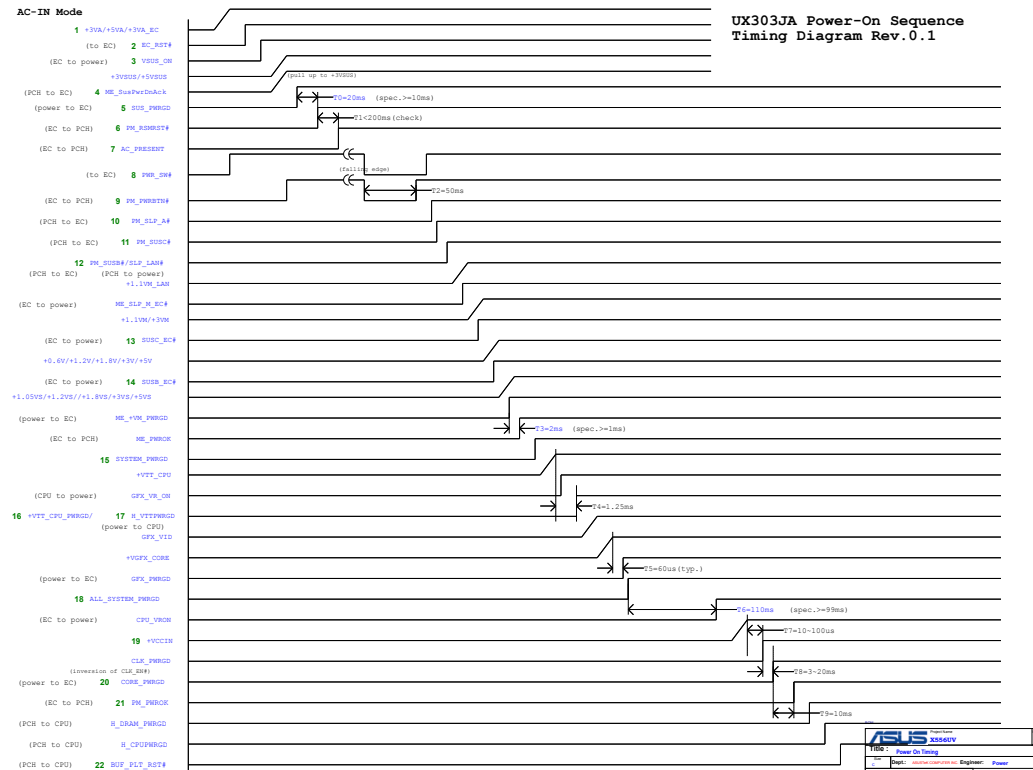
Pre Page

Next Page

MoveTo

AC-IN Mode

1 +30A/+50A/+30A_EC
(to EC) 2 SC_RST#
(EC to power) 3 VSUS_ON
+3VSUS/+5VSUS
(PCR to EC) 4 ME_SnpPwrEnAck
(power to EC) 5 SUS_PWRGD
(EC to PCR) 6 PM_RSTB#
(EC to PCR) 7 AC_PRESDET
(to EC) 8 PWR_ON#
(EC to PCR) 9 PM_PWRBTR#
(PCR to EC) 10 PM_SLP_H#
(PCR to EC) 11 PM_SUSC#
12 PM_SUSM#/SLP_LAM#
(PCR to EC) (PCR to power)
+1.1VM_SAM
(EC to power) ME_SLP_H_R#
+1.1VM/+30A
(EC to power) 13 SUSC_EC#
+0.8V/+1.2V/+1.8V/+3V/+5V
(EC to power) 14 SUSB_EC#
+1.0V/+1.2V/+1.8V/+3V/+5V
(power to EC) ME_VRM_PWRGD
(EC to PCR) ME_PWRGD
15 SYSTEM_PWRGD
+VTT_CPU
(CPU to power) GFX_VR_ON
16 +VTT_CPU_PWRGD/ 17 R_VTTPWRGD
(power to CPU) GFX_VTD
+VDDFX_CORE
(power to EC) 18 ALL_SYSTEM_PWRGD
(EC to power) CPU_VRM#
19 +VDDCIN
(inversion of CLK_DR#)
(power to EC) 20 CORE_PWRGD
(EC to PCR) 21 PM_PWRBOK
(PCR to CPU) R_DRAM_PWRGD
(PCR to CPU) R_CPU_PWRGD
(PCR to CPU) 22 BUF_VLT_RST#

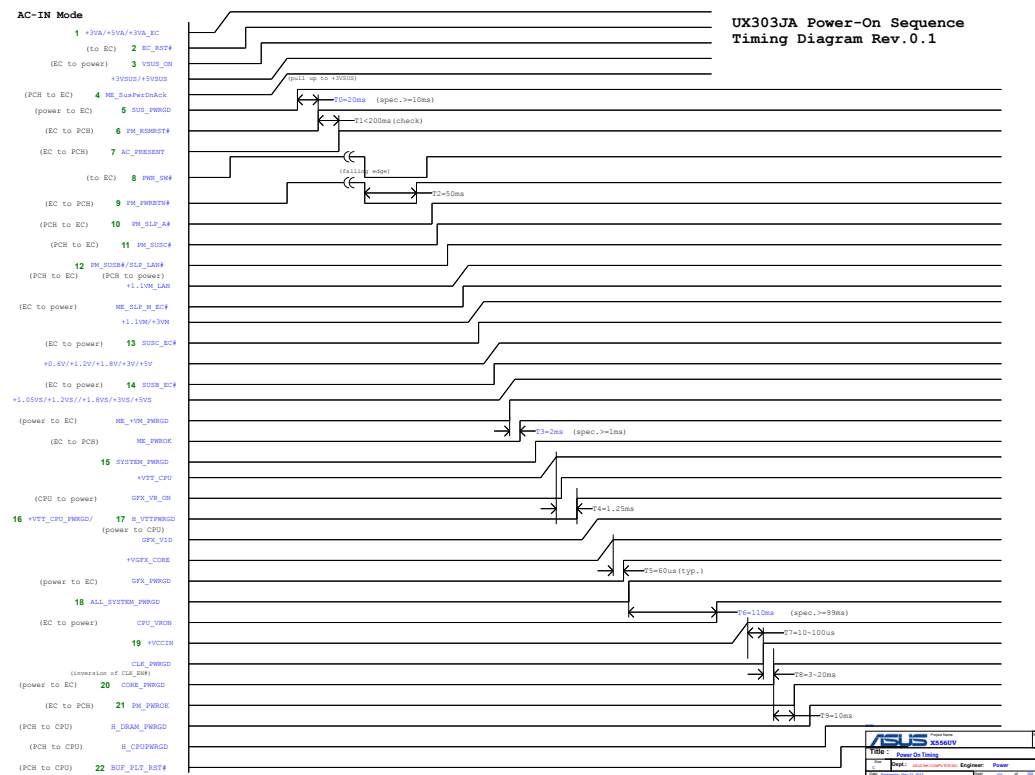
UX303JA Power-On Sequence
Timing Diagram Rev.0.1

Hide

100 Power On Timing--AC mode

101

MoveTo

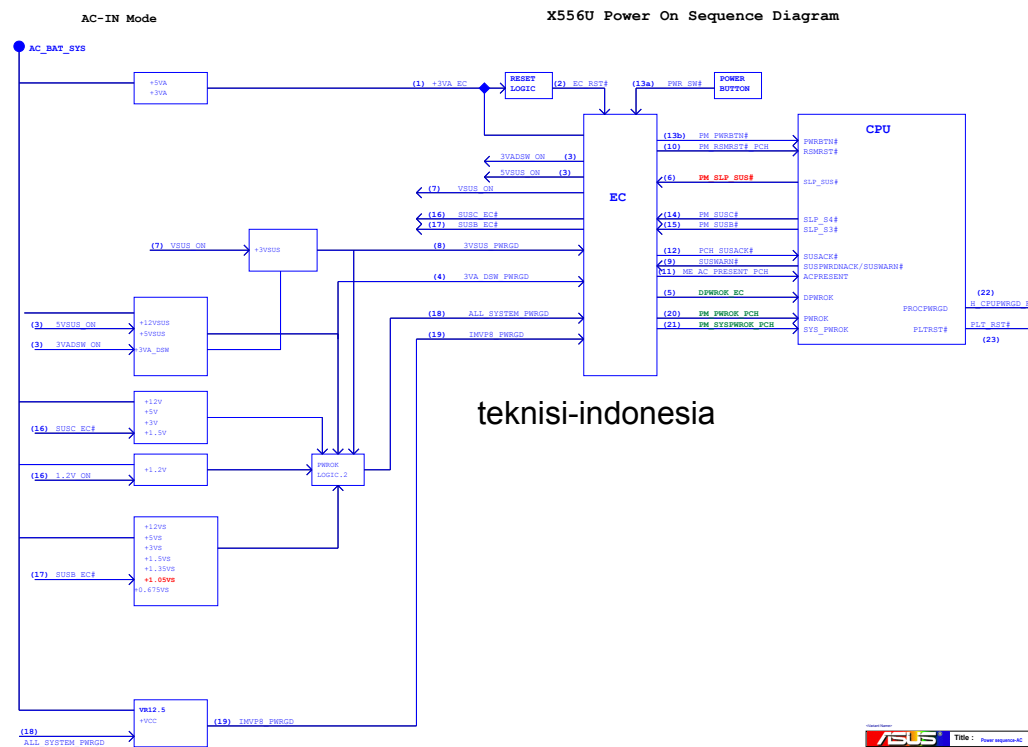


Hide

100_Power On Timing--AC mode

102

MoveTo



teknisi-indonesia

